

JNTU COLLEGE OF ENGINEERING: KUKATPALLY : HYDERABAD
(AUTONOMOUS)
ACADEMIC CALENDER FOR M.S. in VLSI & Embedded Systems Design
COMMENCING FROM 08.08.2012
(JNTUH & SEER AKADEMI)

M.S. in VLSI & ESD - I SEMESTER:

Instructions for Units 1,2,3 & 4	:	08.08.2012	to	28.11.2012	(16Weeks)
Evaluation of End Semester Project	:	30.11.2012	to	07.12.2012	(1Weeks)
Preparation Holidays	:	10.12.2012	to	15.12.2012	(1Week)
End Examinations	:	17.12.2012	to	29.12.2012	(2 Weeks)

M.S. in VLSI & ESD – II SEMESTER:

Instructions for Units 1,2,3 & 4	:	31.12.2012	to	20.04.2013	(16 Weeks)
Evaluation of End Semester Project	:	22.04.2013	to	27.04.2013	(1Weeks)
Preparation Holidays	:	29.04.2013	to	04.05.2013	(1Week)
End Examinations	:	06.05.2013	to	18.05.2013	(2Weeks)
Summer Vacation	:	20.5.2013	to	01.06.2013	(2Weeks)

SUMMER SEMESTER:

Commencement of class work- Summer Semester	:	03.06.2013			
Instructions for Units 1 & 2	:	03.06.2013	to	31.08.2013	(13 Weeks)
Preparation Holidays	:	02.09.2013	to	07.09.2013	(1 Week)
Evaluation of End Semester Project	:	09.09.2013	to	14.09.2013	(1Week)
End Examinations	:	16.09.2013	to	21.09.2013	(1Week)

Date: 14.09.2012


VICE-PRINCIPAL



13

**JNTU COLLEGE OF ENGINEERING: KUKATPALLY : HYDERABAD
(AUTONOMOUS)
ACADEMIC CALENDER FOR M.S. in VLSI & Embedded Systems Design
COMMENCING FROM 30.09.2013
(JNTUH & SEER AKADEMI)**

M.S. in VLSI & ESD – III SEMESTER:

Commencement of Project Phase 1	: 30.09.2013	to	15.02.2014	(20Weeks)
Registration of Project Work	: 14.10.2013	to	25.10.2013	(1 Week)
PRC 1	: 11.11.2013	to	16.11.2014	(1 Week)
Seminar-1	: 16.12.2013	to	21.12.2013	(1Week)
PRC -2	: 10.02.2014	to	15.02.2014	(1 Week)

M.S. in VLSI & ESD – IV SEMESTER:

Project work & Submission	: 17.02.2014	to	14.06.2014	(17 Weeks)
Seminar-2	: 16.06.2014	to	21.06.2014	(1Week)
PRC 3	: 23.06.2014	to	28.06.2014	(1Week)
Project Report/Dissertation	: 30.06.2014	to	05.07.2014	(1 Week)

Date: 30-9-2013


VICE-PRINCIPAL

19