

**JNTUH COLLEGE OF ENGINEERING: KUKATPALLY
(AUTONOMOUS)**

**M.S.(VLSI & ESD) II SEMESTER SUPPLEMENTARY EXAMINATIONS,
MARCH'2014
(JNTUH & SEER AKADEMI)
EXAMINATION TIME-TABLE**

EXAMINATION TIMINGS: 2.00 PM – 5.00PM

| DATE | NAME OF THE SUBJECT |
|-------------------------------|--|
| 18-3-2014 Tuesday | Digital VLSI Design – II Backend |
| 20-3-2014 Thursday | Analog & Mixed Signal IC Design |

**M.S.(VLSI & ESD) SUMMER SEMESTER SUPPLEMENTARY
EXAMINATIONS, MARCH'2014
(JNTUH & SEER AKADEMI)
EXAMINATION TIME-TABLE**

EXAMINATION TIMINGS: 2.00 PM – 5.00PM

| DATE | NAME OF THE SUBJECT |
|-----------------------------|---|
| 24.3.2014 Monday | Mobile Applications Development Digital Signal Processing Image Processing |

Date: 11.3.2014

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VICE-PRINCIPAL