

**JNTUH COLLEGE OF ENGINEERING: HYDERABAD**  
(AUTONOMOUS)

**B.Tech. III year I Semester, Supplementary**  
**Examinations, February, 2015**

**Upto '08' Batch**

**EXAMINATION TIME – TABLE**

**Exam Time: 10.00AM to 01.00 PM**

<b>Date / Day</b>	<b>ECE</b>
23/02/2015	Computer Organisation
25/02/2015	Antennas & Wave Propagation
27/02/2015	Digital Communications
2/3/2015	Digital IC Applications
4/3/2015	Linear IC Applications

**Date:16-02-2015**

**Sd/-**  
**PRINCIPAL**