

JNTUH COLLEGE OF ENGINEERING: HYDERABAD
(AUTONOMOUS)

B.Tech. II year II Semester, Supplementary Examinations,
MAY, 2015

Upto '08' Batch

REVISED EXAMINATION TIME – TABLE

Exam Time: 02.00PM to 05.00 PM

Date / Day	ECE	CSE
18-05-2015	Control Systems	Microprocessors and Interfacing
20-05-2015	Electrical Technology	-
23-05-2015	OOPS Through Java	-
25-05-2015	Switching Theory & Logic Design	-
27-05-2015	EM Waves And Transmission Lines	-
29-05-2015	Analog Communications	-

Date:08-05-2015

Sd/-
PRINCIPAL