

JNTUH COLLEGE OF ENGINEERING: HYDERABAD
(AUTONOMOUS)

B.Tech. II year I Semester, Supplementary Examinations, December, 2015

08' Batch

EXAMINATION TIME – TABLE

Exam Time: 02.00PM to 05.00 PM

| Date / Day | ECE | CSE | METT |
|-------------------|---|-----------------------|----------------------------------|
| 29-12-2015 | Mathematics - III | - | - |
| 31-12-2015 | Pulse & Digital Circuits | - | - |
| 5/1/2016 | Probability Theory & Stochastic Processes | - | Mechanics of Solids |
| 7/1/2016 | Electronic Circuit Analysis | - | OOPS Through JAVA |
| 11/1/2016 | Signals & Systems | Computer Organization | Furance Technology and Pyrometry |

Date:15-12-2015

PRINCIPAL