

Jawaharlal Nehru Technological University, the **First Technological University** of India, was established on 2nd October 1972 in Andhra Pradesh with head quarters located in a historical city **Hyderabad**. The University is one of the premier Universities in India accredited by **NAAC** with ‘**A**’ Grade. After successful and proven levels of appreciated existence and stature spanning over 36 years, JNTU has been divided into four different universities by Govt. of Andhra Pradesh, through Act No.30, Dt. 24th September, 2008. One of the constituent colleges of the University “**JNTUH College of Engineering, Hyderabad**” is regarded as a pioneer in shaping the excellence of some of the leading organizations of the industry, by churning out the finest professionals with a resolve to scale greater heights in the technological scenario, every year. Other constituent college of JNTUH is located at Jagityal and 11 other academic units at Hyderabad campus.

The Department of **Electronics and Communication Engineering** established in 1973, is instrumental in molding the careers of students and helping them to become world-class professionals. The department is offering UG, PG, Research and Collaborative Programmes with well experienced faculty and as well as established laboratories. Besides highly qualified and experienced staff and well-equipped laboratories, the Department has been awarded **8.1** points out of **10** by the **State Board of Technical Education**.

About the Workshop:

This workshop is the first of the series to be organized to give an understanding on Research trends in the areas of VLSI & ES, for the benefit of faculty & students, under TEQIP-II. This workshop provides the future scholars and researchers a vision into recent trends in VLSI & Embedded Systems.

Resource Persons:

Resource persons are drawn from premium institutes like IIT Hyderabad, BITS Hyderabad, JNTUH, Industry and R & D centers like RCI, C-DAC and RedPine Systems.

Eligibility:

Students and Faculty of Educational Institutions and Persons working in R& D Organization, with a minimum qualification of Bachelor’s degree in ECE / CSE / EEE and allied branches of Engineering, are eligible to apply.

Registration: FREE

Last date for registration: 4th November, 2016 by 3:00 p.m.

Address for Correspondence

Dr. M. Asha Rani

Professor & Head

Department of ECE, JNTUH CEH

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Venue: ECE Dept. , JNTUH CEH

Time: 10:00 am to 4:00 p.m

Note : All the participants are requested to be present by sharp 9:30 A.M on 05th November , 2016. All the Registration Forms should reach the above mentioned e - mail id.

Registration Form

Name of the Applicant:.....

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Designation:.....

Gender: (M/F):

Educational Qualifications:.....

Address for Correspondence : (Including E-mail, Fax, Cell / Landline)

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Name of the Sponsoring Institute/Organization:

Signature of the Applicant with date

A One - Day Workshop on

**“Research Trends in VLSI
& Embedded System”**

05th November, 2016

Under TEQIP - II

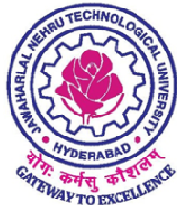
Coordinators

Dr. M. Asha Rani, Prof. & Head

Dr. M. Madhavi Latha, Professor

Department of ECE

JNTUH CEH



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