

Curriculum Vitae



Name: **Dr. Makkena Madhavi Latha**
Mother's Name: Smt. Makkena Devaki Devi
Father's Name: Late Sri. Makkena Krishna Murthy
Husband's Name: Sri. Lavu Tirumaleshwara Prasad
Date of Birth: 01-06-1966

Address for Communication

Dr. Makkena Madhavi Latha
Professor in ECE &
Former Director, Innovative Technologies
JNTU University Hyderabad
Kukatpally, Hyderabad – 85
Telangana State, INDIA

Permanent Address

Dr. Makkena Madhavi Latha
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Telangana State, INDIA

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mmadhavilatha@jntuh.ac.in

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Personal: Dynamic, Ambitious, Computer savvy, Adaptive & a keen learner, Optimistic Personality with a positive outlook, Hardworking, Honest & Humble.

Educational Qualifications:

1. **Ph. D** (Wavelet Transforms & Their Applications to Various Types of Signals) - JNTU – 2002
2. **M. Tech** (Digital Systems & Computer Electronics) – First Class with Distinction – JNTU – 1993
3. **B. E** (E. C. E) –First Class – Nagarjuna University - 1986

Awards Received:

National:

1. Received **State Best Teacher** Award from Govt. of Andhra Pradesh in 2013- **First Woman Professor in Engineering in united Andhra Pradesh.**

International:

1. IEEE Women in Engineering Appreciation
2. Exchange Visitor Award, USA
3. Best Paper Presentation Award-WAIRCO, Sri Lanka, 2015
4. External Examiner to Harare Institute of Technology, Zimbabwe

Other Qualifications:

- Certificate Course in UNIX & C Languages
- Certificate Course in VHDL Programming
- Knowledge on MATLAB Programming and development of various algorithms using MATALB
- Knowledge on Verilog Programming and FPGAs
- Practical knowledge on TMS320C54xx & 6711 Processors
- Knowledge on Python Programming
- Hands-on knowledge on NI Labview & ELVIS-II

Experience:

1. Worked as Lecture in C. R. Polytechnique, Ch'Pet, Guntur for 3 years
2. Worked as Engineer in Powertronics, Hyderabad for 1 year
3. MID-Field Steels Pvt. Ltd., Hyderabad for 1 year
4. Worked as Graduate Engineer in E. C. I. L, Hyderabad for about 1 year
5. Joined as Assistant Professor in JNTU on 4th October, 1994
6. As Assistant Professor in JNTU, Hyderabad from 4th Oct 1994 31st May 2003
7. As Associate Professor in JNTU, Hyderabad from 31st May 2003 – 9th November 2006
8. Guest Faculty for UG Course at I.I.I.T, Hyderabad during 2002 & 2003
9. Working as Professor in JNTU CEH from 9th November 2006 to till date

Total experience: 26 Years

- In teaching: 22 (JNTUH) years
+ 3 (CR Polytechnique College) years&
- In Industry: + 3 years.
- Research Experience: 15 Years

R & D Projects Handled/Handling: 04

- AICTE Projects Handled: Principal Investigator for AICTE Sponsored TAPTEC Project on “Digital Signal Processing” – 2000 for **Rs 3 – 00 Lakhs.**
- Principal Investigator for AICTE MODROBS Project on Communication Laboratory for **Rs. 8-00 Lakhs**
- Coordinator/Principal Investigator for DST-FIST Project for **Rs. 55 - 00Lakhs** during November 2012 in ECE Department, JNTUH CEH
- *Linneaus Palme project for Bilateral Faculty Exchange*, funded by Swedish Council for Higher Education, Sweden - 80000 SEK (sanctioned in 2014 duration of 2 years)

New Laboratories and Centre of Excellences established:

1. CVED
2. CEAT
3. ICC
4. BS Lab
5. BEE Lab
6. e-LSDM studio

R & D Consultancy:

- Development of Solar Desalination System with M/s. Oorja Energy Engineering Services, Hyderabad.

Research Experience / Field of Interest:

Guiding Ph. D Students in the area of:

- Signal Processing
- Image Processing
- Speech Processing
- Biomedical Signal Processing
- Wavelet Transform Applications
- Mojette Transform Applications
- VLSI
- Analog & Mixed Signal Design
- Low Power VLSI
- Embedded Systems
- DSP Processors & Controllers
- Communications

Membership of Professional Bodies:

1. Life Member MISTE
2. Fellow of IETE – F169872
3. Member of IEEE
4. Chairperson, IEEE Women in Engineering Society, Hyderabad Section, 2010

Subjects Taught: 47 Subjects

1. Electronic Devices & Circuits
2. Electronic Circuits
3. Pulse & Digital Circuits
4. Power & Industrial Electronics
5. Basic Electronics
6. Network Theory
7. Optimization Techniques
8. Computer Organization
9. "C" Programming & Data Structures
10. C++ Programming
11. Programming Methodology & Data Structures
12. Microprocessors & Digital Electronics
13. Systems Programming
14. Advanced Operating Systems
15. Linear & Digital IC Applications
16. Networks, Signals & Systems
17. Signals & Systems
18. Transform Techniques
19. Digital Signal Processing
20. Digital Image Processing
21. Pattern Recognition
22. Digital Signal Processors & Architectures

23. VLSI Technology & Design
24. Speech Processing
25. Biomedical Signal Processing
26. Random Processes and Time Series Analysis
27. MATLAB Programming
28. Low Power VLSI Design
29. CMOS Analog Mixed Signal Design
30. Adaptive Signal Processing
31. CMOS Analog IC Design
32. CMOS Digital IC Design
33. Advanced Digital Signal Processing
34. 4G Technologies
35. Digital Systems Design Laboratory
36. Digital Communications Laboratory
37. Signal Processing Laboratory
38. Simulation Laboratory
39. CPDS Laboratory
40. Digital Signal Processing Laboratory
41. EDC Laboratory
42. LDIC Laboratory
43. PDC Laboratory
44. Electronic Circuits Laboratory
45. C++ Laboratory
46. E-CAD Laboratory
47. Digital System Design Laboratory

M. Tech Projects Guided: 217 (List Enclosed)

Ph.Ds Guided: 20

1. Ms. Arti Khaparde- Independent Component Analysis for Images and its Application to Content Based Image Retrieval (2008)
2. Ms.G. Lakhmi Madhumati – Design and Modelling of Two-Step Flash ADC in 180nm CMOS Technology – 2010-11
3. Mr. M. Satya Sairam – Hybrid Vector Quantization for Low Bit-rate Speech Coding Applications – 2010-11
4. Mr. V. Venkat Rao – Development of Multipolynomial, Minkowski Function based Navigation Solutions and the Analysis of Satellite Clock and Relativistic Error Impact on the Navigation Solution for Precise Position Applications – 2011
5. Ms. S. M. Usha –Modeling and Simulation of Biosensor Arrays for Automated Cancer Detection - Dec 2012
6. Mr. Kesavan Pillai –Hardware Implementation of Video Water Marking Algorithms, March 2013.
7. Ms. Esther Rani – Design of Modified Self-Sleep Buffer For Distributed MTCMOS Technique in Arithmetic and Fast Computations- 11-03-2014
8. Mr. M. Suman – 10304004 - - Enhancement of Compressed Noisy Speech Signal”- 3 July, 2014- KL university, Vaddeshwaram
9. Mr. K. Hari Kishore – 10304008 – Fault Free Error Coding Technique for Memory Applications- 21 July, 2014- KL University, Vaddeshwaram

10. Mr. Ch. Raja-0503PH0817 – Off-line Analysis and Estimation of Radar Signal Parameters for creating Electronic Order of Battle Record - 8th Aug, 2014
11. Ms. K. Padma Vasavi – 0803PH0877 – Multi Scale Multi Directional Edge Detection using Statistical Thresholding – August, 2014.
12. Ms. A. G. Padma – 410845EC/PH -ECG Monitoring System for Detection of Arrhythmias and Minimization of Storage requirements using Compression Techniques – November, 2014
13. Mr. M. Manoj Kumar - 0603PH0849 -Real Time Optical Logical Controller using Fuzzy Systems - September, 2015.
14. Ms. P. A. Harshavardhini - 0703PH0858 - "High Performance and Reconfigurable On-chip Sigma Delta ADC Architecture for Next Generation FPGA Applications", September, 2015.
15. Mr. A. Satish - 0803PH08A46 - Efficient Technique to Minimize Dynamic Power and Crosstalk Delay on On-Chip Data Bus in Deep Submicron Technology - October, 2015.
16. Mr. Dathatreya - 0803PH0899 - Intelligent Image Compression and authentication using Artificial Neural Networks, November, 2015.
17. Ms. L. Leela Rani - 1103PH0827 - "Investigations on Optimization of Leakage Power using Efficient Leakage Power Reduction Techniques for CMOS VLSI Circuits" , 27 May, 2017.
18. Mr. Alimelu Manga - 0803PH08A32 - "Optimal Word Length Algorithm using Multiple Word Length Approach based on SNR Constraints for Low Power" , 25 May, 2017.
19. Mr. N. Udaya Kumar - 0803PH08A16- Multi Scale and Multi Directional Region of Interest Based Scheme for Image Compression, 26 May, 2017.
20. Mr. Anwar Basha Pattan- 1203PH0802 - "Optimization of Single-path Delay Feedback FFT Architecture for Low Power", 1 September, 2017.

Patents Filed:

1. Dr. MML et al, Ref No. 201841002292, Application No. TEMP/E1/1408/2018-CHE, CBR No. 1734 - "Physiological Data Monitoring and Notification System and Method Employed thereof"- Dr. K. Hari Kishore, Dr. Habibulla Khan,KLU and Dr. M. Madhavi Latha, JNTUH ,Controller General of Patents, Designs & Trade Marks, India, 2018.
2. Dr. MML et al, Ref No. 201841002291, Application No. TEMP/E1/1412/2018-CHE, CBR No. 1733 - "Implantable Medical Device Actuated by Piezoelectric effect"- Dr. K. Hari Kishore, KLU and Dr. M. Madhavi Latha, JNTUH, Controller General of Patents, Designs & Trade Marks, India, 2018.

Ph. D Thesis Submitted: 01

1. Ms. Anitha -

No. of Ph.Ds Guiding: 04

Administrative Experience: 24

1. **Officer In-charge Examinations** from October, 2003 to March 2007 for 3.5 years– **First Woman** Officer in Examinations
2. **Coordinator, Center for VLSI and Embedded Systems Design (CVED)**, JNTUH CEH from 2010 to till date
3. **Professor I/c Library** for two years during 2010 to 2012
4. **Head of the Department of Electronics & Communication Engineering** from 13 September 2010 to 26 September 2012 - **First Woman ECE Head in** JNTUH.
5. **University Coordinator** between JNTUH & Blekinge Institute of Technology, Sweden for MOU course.
6. **Chief Superintendent for conducting EAMCET-2010 in JNTUH CEH**
7. Conducted **EAMCET, ECET, PGCET and many competitive Examinations** as Officer In-Charge Examinations
8. **Convener, Forest Department Recruitment Test (FDRT) -2014, JNTUH**, which is very complex test as 4 times EAMCET Examination, for the posts of FSO, FBO, ABO, TH, BW & TA- **First Woman Convener in the state.**
9. **Co-Convener, EAMCET-2014- First Woman Co-Convener in the state.**
10. **Convener for Regular Faculty Recruitment Test (FRT)-2014 in JNTUH, 2014 - First Woman Convener in the state.**
11. **Convener for e-LSDM Project for implementing e-Learning Solutions & 2-way HD delivery Mechanism for Faculty and Students, at JNTUH - First Woman Convener in the state.**
12. **Chairperson, Board of Studies for ECE & ETM board, JNTUH**
13. **Standing Council Member for JNTUH for grant of JNTUH affiliation.**
14. **Expert member for many of Competitive Examinations like Orissa PG CET, AP PGCET, APPSC and other CETs.**
15. Established **Centre of Excellence in Automation Technologies (CEAT)** in collaboration with Bosch Rexroth, Germany, first time in the state.
16. Established **Innovative Computational Centre** at D-IT, JNTUH.
17. Established **Centre of Excellence in VLSI Design & Embedded Systems Design (CVED)** in collaboration with Cadence Design Systems, Bangalore, Synopsys, USA, Mentor Graphics, Bangalore at JNTUH CEH.
18. **In-Charge** for infrastructure facilities in Class Room Complex, JNTUH
19. Established **e-LSDM studio for on-line interactive video lectures** to the students of JNTUH affiliated Colleges.
20. **Chairperson, Board of Studies for BME/EIE/ICE/ECM board, JNTUH- Present.**
21. **Chairperson, Board of Studies for ECE & ETM board, JNTUH- 2012-2014.**
22. **Design Contest Chair** in an Silver Jubilee International Conference on VLSI Design conducted during 7-11 January, 2012.
23. **Co-Chairman of Tutorials in PrimeAsia 2015-An International conference on Post Graduation Research in Microelectronics and Electronics, a joint conference of**

- CAS & EDS and IEEE Hyderabad Section organized at Vasavi College of Engineering, Hyderabad during 27-29 November, 2015.
24. **Director, Innovative Technologies**, JNTUH from 2013-2016.

Conferences Conducted: 07

1. Organized SPOORTHI'05, a National Level Technical Student Paper Contest on 2-03-2005
2. Design Contest Chair in an Silver Jubilee International Conference on VLSI Design conducted during 7-11 January 2012
3. Organized SPOORTHI'13, a National Level Technical Student Paper Contest on 14-03-2013 & 15-03-2013.
4. **Convener** for a 3-day 2nd International Conference on Recent Advances in Design, Development and Operation of Micro Air Vehicles (ICRAMAV-2013) during 21-23 November 2013 (Chief guest: Dr. Avinash Chander, SA to Raksha Mantri, Govt. of India).
5. **Convener** for a 2-day First National Conference on Robotic, Automation and Mechatronics (NCRAM-2014) during 12-13 March 2014.
6. **Convener** for a 3-day 3rd International Conference on Recent Advances in Design, Development and Operation of Micro Air Vehicles (ICRAMAV-2014) during 5-7 November 2014 (Chief guest: Dr. G. Sateesh Reddy, Director, RCI, Hyd.).
7. **Convener** for a One day National Seminar on Innovative Technologies (NSIT)-2014 on 7th October, 2014 (Chief Guest: Bharat Rant Padmasri Dr. A.P.J. Abdul Kalam, Former President).

Academic Council Member/ Board of Studies Member /Subject Expert Member:

1. BOS Chairperson- ECE/ETM, JNTUH, 2012-2014
2. BOS Chairperson- BME/EIE/ICE/ECM, JNTUH, 2014-2016
3. BOS member for ECE & ETM, BME,during 2007-2012, JNTUH
4. BOS member for ECE , JNTUH CEH, 2008-2010
5. JNTUH CEH BOS Chairperson for ECE during 2010-2012
6. Academic Council member for KL University, Guntur
7. Academic Council Member of SNIST, Ghatkesar
8. BOS Member for ECE , KL University & E.Comp.E, KL University
9. BOS Member for E.Comp.E, KL University
10. BOS Member for Satya Bhamadeemed University, Chennai during 2014
11. BOS Member for GRITE, SR Engg. College, JBIT.
12. Governing Body Member of JBIET, Chilukuru, Hyd.
13. Judge for Research Category in NI's Educators Day conducted during 16-17 October 2012 at Chennai
14. Subject Expert for DRDO interviews
15. Subject Expert for RCI interviews
16. Subject Expert for NRSC interviews
17. Subject Expert for APPSC interviews
18. Subject Expert for UPSC interviews
19. External Member in the Screening Committee for JRF Recruitment at DLRL, Hyderabad, Feb 2018.

Advisory Member:

1. Two day National Conference on Emerging Trends in Signal Processing Embedded Systems, 9-10 February, 2012

International Exposure & Activities:

1. University Level Coordinator between JNTUH and Blekinge Institute of Technology, Sweden since 2007.
2. Nominated by the U. S Consulate general Hyderabad for the proposed exchange program to promote better cooperation between educational institutions in the U.S. and India on the Institute on **'Industry-Private Sector Linkages & Innovation '**.
3. Visited Foreign Universities/countries for academic and R&D interaction:
 - Blekinge Institute of Technology, Sweden
 - University of California, Berkeley
 - San Jose University, USA
 - Stanford University, USA
 - Linköping University, Sweden
 - University of Rome, Rome
 - Innsbruck, Austria for paper presentation
 - Bosch Rexroth, Germany
 - Denmark, Venice, Finland, Singapore, China, Hongkong, Bangkok as visitor
4. MOU with **Bosch Rexroth, Germany** for establishing Centre of Excellence in Automation Technologies (CEAT) at JNTUH worth **INR 4.00 Crores** on 50:50 sharing base.
5. MOU with Synopsys Inc, USA, for Software donation worth **\$150 million**.
6. Appointed as External Examiner to The **Harare Institute of Technology, Zimbabwe** by its Executive Board.

National MOUs Established:

1. Cadence Design Systems, Bangalore –Rs. 20-00Lakhs worth EDA software as free to CVED
2. Mentor Graphics, Bangalore - Rs. 20-00Lakhs worth EDA software as free to CVED

International MOUs Established with:

1. Bosch Rexroth, Germany for establishing Centre of Excellence in Automation Technologies (CEAT) at JNTUH
2. Synopsys, USA – USD 150 Million worth EDA software as free to CVED.
3. Texas Instruments, USA – Established First Digital Signal Processing Laboratory for IV ECE – Received 5 DSKs as donation
4. Analog Devices, USA - Received 5 DSKs as donation

Ph. D. Adjudicator/Evaluator/Reviewer:

1. Anna University – Ms. C. Gnana Kousalya – Secured Energy Efficient Key Management Scheme for Wireless Sensor Network.

2. Reviewer of IC on Signal Processing, Pattern Recognition and Applications (SPPRA 2012) June 18-20, 2012, Crete, Greece.
3. Andhra University – Mr. M. N. V. S. S. Kumar – Development of Submarine/AUV under Navigation Techniques using Imaging Sonar
4. Ph. D adjudicator for:
 - i. Anna University
 - ii. Satya Bhama Deemed University
 - iii. Sri Satyasai Deemed University
 - iv. Punjab University
 - v. Andhra University (AU)
 - vi. Hyderabad Central University (HCU)
 - vii. Osmania University
 - viii. Sri Krishnadevaraya University (SKU)
 - ix. Karunya University.
 - x. Gitam University, Vizag

Book Chapter: 01

1. K. Padma Vasavi, Dr M. Madhavi Latha, Dr E.V.Krishna Rao, N. Udaya Kumar “Combining Multi Scale and Multi Direction Analysis in Edge Detection Using Statistical Thresholding”, book chapter in the research book titled “Intelligent Automation and Systems Engineering”, **Springer Veralag, 2011.**

Books Authored: 01

1. Signals, Systems and Communications – B. P. Lathi & M. M. Latha, BSP Publications, 2012.

Total No. of Papers (Journals & Conferences): 177

National Journals:	07	International Journals:	80
National Conferences:	23	International Conferences:	67

Journal Papers:

2007

1. “Musical Noise Removal of Enhanced Speech using Gray level values of Spectrogram plots” is published in ECTI Journal of Electrical Electronic and Communications (ECTI-EEC) vol.5, No.2 August 2007, pg: 137-146 K.Anitha Sheela, Dr. K. Satya Prasad and Dr. M. Madhavi Latha .

2008

2. M. Madhavi Latha , M. Ramana Reddy, “Blind Channel Estimation using Bayesian approach For Wireless Communication” i-manager's Journal on software Engineering Vol. 2 No. 3 January – March 2008.
3. M. Madhavi Latha, Arti Khaparthi, "Content Based Image Retrieval Using Independent Component Analysis"- IJCSNS International Journal of Computer Science and Network Security, 2008
4. M. Madhavi Latha, Arti Khaparthi,“Mixed Image Separation using FASTICA” – Published by WSEAS press- proceedings of the 7th WSEAS International

- Conference on Signal Processing (SIP '08)- May 27-30, 2008- p. No 145-149
5. M.Madhavi Latha, M. Satya Sai Ram et al "Multi Switched Split Vector Quantizer" - International Journal of Computer, Information, and Systems Science, and Engineering Volume 2 Number 1,2008

2009

6. IJCE 2009- Power And Delay Analysis of A 2-To-1 Multiplexer Implemented In Multiple Logic Styles For MUX-Based Decoder In Flash ADC- Gummadi Lakshmi Madhumati, Dr.M.Madhavilatha and Mr.K.Ramakoteswara rao koneru – No 281
7. Design of CMOS comparators for FLASH ADC – IJ of Electronics Engineering (IJEE), vol. 1, No.1, Jan-June 2009, pp53-57 – M. Madhavi Latha, G. L. Madhumati
8. "Review of Low Noise CMOS Latched Comparator for High Speed Flash Analog - to - Digital Converter," GITAM Journal of Information Communication Technology, Vol.2, No.1, January-July-2009, pp.51-56, M. Madhavi Latha, G.L.Madhumati (**National**)
9. "Power and Delay Analysis of a 2 – to - 1 Multiplexer Implemented in Multiple Logic Styles for Multiplexer-Based Decoder in Flash ADC," International Journal of Recent Trends in Engineering (Electrical and Electronics), Vol.1, No.4, May 2009, pp.29-31, M. Madhavi Latha, G.L.Madhumati
10. "ECG compression and Lab view implementation", International Journal of Bio-medical Science and Engineering, 2009, 2177-183, M. Madhavi latha, T. Padma, Abrar Ahmed
11. "A Novel Architecture of Hybrid Image Compression Model based on Reversible Blockade Transform", International Journal of Electronics, Circuits and Systems (IJECs)-, WASET (World Academy of Science, Engineering and Technology, July 25, 2009, C. Hemasunder, Dr. M. Madhavi Latha
12. "Decision making Algorithm through LVQ Neural Network for ECG Arrhythmias" Chwee Teck Lim, James C. H. Goh (Eds):ICBME 2008, IFMBE proceedings Vol 23, pp 85-88, 2009, www.springerlink.com, M. Madhavi Latha, T. Padma, K. Jaya Kumar
13. "Compression techniques for ECG transmission" Proceedings of ICBENT 2008 as special issue of Medical Journal of D. Y. Patil University MDYPU Vol-II issue No: 3, 2009, ISSN No: 0974-2743 M. Madhavi Latha, T. Padma,
14. "A Novel VLSI Architecture of Hybrid Image Compression Model based on Reversible Blocked Transform", International Journal of Electronics, Circuits and Systems, 3-1-09, P.No.25-31, C. Hemasunder, Dr. M. Madhavi Latha
15. „Disease Detection using Pattern Recognition Techniques:- NC 0m Emerging Trends in Information Communication Technology (ETICT-08), GITAM University, 19-20 December and published in GITAM Journal of Information Communication Technology, Vol.2. Jan-July 2009, Pno. 34-37 (Awarded as the Best Paper) (**National**)
16. "Automated disease detection and drug diffusion system", European Journal of Scientific Research (EJSR)- S. M. Ushaa. M. Madhavi Latha, G. Madhusudhan Rao (Accepted)– 2009

17. A.Sathish, M.Madhavi Latha, K.Lal Kishore, T.Jaya Chandra Prasad “An Efficient Data-Bus Encoding Technique to Reduce Coupling Transitions in DSM Technology” Technology Spectrum Vol.3. No.2. JULY , 2009, pp22-28, (ISSN: 0974-6854).**(National)**
18. A.Sathish, M.Madhavi Latha, K.Lal Kishore, M.V.Subramanyam, C. S.Reddy“ A Technique to Reduce Data Bus Coupling Transitions in DSM Technology” i-manager’s Journal on Software Engineering, Vol. 4. No. 2. October- December 2009, pp67-73, ISSN Print: 0973-5151, ISSN Online: 2230-7168.**(National)**
19. G. L.Madhupati, M. Madhavi Latha, K. Rama Koteswara Rao- : Design of CMOS Comparators for FLASH ADC”, IJ of Electronics Engineering, 1(1), 2009, pp. 53-57, Vol 1 Number 1, January-June 2009, ISSN:0973-7383
20. M. Satya Sai Ram, M.Madhavi Latha, et al “Usefulness of Speech Coding in Voice Banking” – Signal Processing International Journal (SPIJ), Volume 3, Book:2009, Issue 4, Date:31-08-2009, ISSN:1985-2339, P. No:42054

2010

21. “An Efficient Data-bus Encoder Techniques to Reduce Coupling Transitions in DSM Technology”, International Journal, Technology Spectrum, JNTUH- A. Satish, Dr. M. Madhavi Latha, Dr. K. Lal Kishore, Dr. T. Jaya Chandra Prasad – Vol 3, No 2 July 2010
22. A.Sathish, M.Chennakesavulu, M.Madhavi Latha, K.Lal Kishore. “Performance Analysis of Hamming Code for Fault Tolerant 8-Bit Data Bus in VDSM Technology” i-manager’s Journal on Software Engineering, Vol. 4. No. 4. April-June 2010, pp55-60.
23. B.Chinnarao, Dr.M.Madhavilatha, “Analysis of Multi Resolution Image Denoising Scheme using Fractal Transform”, The International Journal of Multimedia & Its Applications (IJMA) Vol.2, No.3, August 2010. pp. 63-74.
24. B.Chinnarao, Dr.M.Madhavilatha,"A Combination of Wavelet & Fractal Image Denoising Technique”, International Journal of Electronics Engineering(IJEE), Volume 2, Number 2,July-December 2010,pp 259-264.
25. Kesavan Gopal, Dr. M. Madhavi Latha – “Watermarking of Digital Video Stream for Source Authentication – IJCSI International Journal of Computer Science Issues, Vlo 7, Issue 4, 2010, ISSN (online): 1694-0784, ISSN (print): 1694-0814, pp. 18-25.
26. C. Hema Sunder Rao, Dr. M. Madhavi Latha – “ A Novel VLSI Architecture of Hybrid Image Compression Model based on Reversible Blocked Transform”, WASET (World Academy of Science, Engineering and Technology)-IJECS, pp.572-578
27. K. Padma Vasavi, M. Madhavi Latha et al – “Performance Evaluation of an Adaptive Statistical Thresholding in Edge Detection using Gray Level Co-occurrence Matrix in Wavelet Domain under Noisy conditions”, ICGST- GVIP Journal, Volume 10, Issue 3, August 2010, P. No: 35 to 44. **(National)**
28. K. Padma Vasavi, M. Madhavi Latha et al – “Combining Multiscale and Multidirectional Analysis for Edge Detection using a Statistical Thresholding”, Book Chapter published in the Lecture notes on Electrical Engineering titled

- “Intelligent Automation and System Engineering”, **Springer Publications**, P. No: 325 to 337
29. K. Padma Vasavi, M. Madhavi Latha et al – “A Novel Statistical Thresholding in Edge Detection using Laplacian Pyramid and Directional Filter Bank”, International Association of Engineers (IAENG) Lecture Notes in Engineering and Computer Science, Volume 1, P. No: 589-593, 2010
 30. V. Venkata Rao, G. Sasibhushana Rao and M. Madhavi Latha – “Receiver Position Error Analysis using Point Solution Approach Algorithms”, The IUP Journal of Telecommunications, Vol. II, No. 4, November, 2010, P.No: 33-39.
 31. V. Venkata Rao, G. Sasibhushana Rao, M. Madhavi Latha and M. N. V. S. S. Kumar – “ GPS Position Error Analysis for Precise Surveying and GAGAN Applications Over the Indian Subcontinent”, The Journal of Indian Geophysical Union, Volume 14, No. 4, October, 2010, P.No: 259-264
 32. V. Venkata Rao, G. Sasibhushana Rao, M. Madhavi Latha, Y. Ravi Kumar and K. V. N. M. Prasad – “Analysis of Relativistic Error Effect on the GPS Time and the Receiver Position Accuracy”, The CIIT International Journal of Wireless Communication, Vol. 2, No. 9, September, 2010, P. No: 318-324.
 33. M. Suman, Habibulla Khan, M. Madhavi Latha and K. Madhuri – “ A New Algorithm for Analysis of Speech Signal Based on Pitch and Impulse Response Spectrum”, International Journal of Systems and Technologies, Vol 3, Number 2, 2012, pp. 227-240.

2011

34. A Sathish, MM Latha, KL Kishore “Efficient Switching Activity Reduction Technique for Fault Tolerant Data Bus,” International Journal of Computer Applications, Vol.36 (12), PP.7-12, 2011, Published by Foundation of Computer Science, New York, USA. (DOI: 10.5120/4546-6346)
35. A.Sathish, M.Madhavi Latha, K.Lal Kishore “A Technique to Reduce Transition Energy for Data-Bus in DSM Technology” IJCSI International Journal of Computer Science, Vol. 8, Issue 4, No 2, July 2011, PP.402-406, (ISSN (Online): 1694-0814)
36. A.Sathish, M.Madhavi Latha, K.Lal Kishore “ An Efficient Switching Activity Reduction Technique for On-Chip Data-Bus” IJCSI International Journal of Computer Science, Vol. 8, Issue 4, No 2, July 2011, PP.407-413, (ISSN (Online): 1694-0814)
37. A.Sathish, M.Madhavi Latha, K.Lal Kishore “Efficient Crosstalk Reduction Technique for Data-Bus”. IJCA International Journal of Computer Applications, Vol.28, No.11, August 2011, PP.37-40, Published by Foundation of Computer Science, New York, USA. (DOI: 10.5120 /3520-4715).
38. B. Chinna Rao, Dr.M. Madhavi Latha, “Reconfigurable Wavelet Thresholding for Image Denoising while Keeping Edge Detection” in International Journal of Computer Science and Network Security (IJCSNS), VOL.11 No.3, March 2011.
39. B.Chinnarao, Dr.M.Madhavilatha, “ Selective Neighbouring Wavelet Coefficients Approach for Image Denoising”, International Journal of Computer

- Science and Communication (IJCS), Volume-II, Number-I of January-June 2011, pp.73– 77.
40. B.Chinnarao, Dr.M.Madhavilatha, “Second Generation Curvelet Shrinkage Model Based Image Denoising”, International Journal of Computer Science and Communication (IJCS), Volume-II, Number-I of January-June 2011, pp. 279-285.
 41. B.Chinnarao, Dr.M.Madhavilatha, “Image Compression and Decompression using Discrete Wavelet Transform”, Global Journal of Computer Application and Technology (GJCT) ,Vol 1 (1), 2011, 66-71.
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71. A.Sathish, M.Madhavi Latha, K.Lal Kishore, Y.Praveen Kumar Reddy “Energy Efficient Encoding Technique for Data-Bus in DSM Technology” IEEE international conference on Signal Processing, Communication, Computing and Networking Technologies (ICSCCN 2011), August 2011. Page(s): 490 – 492,(DOI: 10.1109 /ICSCCN. 2011. 6024600).
72. A.Sathish, M.Madhavi Latha, K.Lal Kishore, K.K.Reddy “Crosstalk Reduction Technique on Data-Bus in DSM Technology” IEEE international conference on Signal Processing, Communication, Computing and Networking Technologies (ICSCCN 2011), August 2011. , Page(s): 486 – 489.
73. P. Srinivasa Rao & M. Madhavi Latha – “Morphological filters usng structuring elements and their use in image shaping”, ICSCI-2011, Hyderabad

2012

74. N.Udaya kumar, K.Padma Vasavi, Dr. M.Madhavi Latha, Dr. E.V.Krishna Rao, “An ROI Based Image Compression system for SAR Imagery in Micro unmanned Air Vehicles for Border Security”, ICRAMAV-2012, December 10-12, 2012 JNTUH Campus,Hyderabad.
75. P. Srinivasa Rao & M. Madhavi Latha –“On the notion of valid morphological filters and their applications”, ICVSP-2012, Bangalore.
76. S. Latha & M. Madhavi Latha -“Area efficient architecture for Frequency domain multi-channel digital down conversion for randomly spaced signals” in an International Conference ACITY-2012.

77. Harshavardhini & M.Madhavi Latha – “Performance analysis of first order digital Sigma Delta ADC”, IEEE 2012 Fourth International Conference on Computational Intelligence, Communication Systems and Networks CICSyN2012, , pp.435 – 440, 24th – 26th July, 2012. (Print ISBN: 978-1-4673-2640-7), Phuket, Thailand
78. Harshavardhini & M.Madhavi Latha – A 65dB SFDR, 500 kS/s Continuous Time All Digital Sigma Delta ADC for SONAR Applications”, International conference- Pearl Jubilee International Conference on Navigation and Communication (NAVCOM-2012), pp. 328-331, Dec 2012, OU & DRDO, Hyderabad.

2013

79. N. Udaya Kumar, Dr. E. V. Krishna Rao, Dr. M. Madhavi Latha, K. Padma Vasavi, “A Region of Interest Based Direction Sensitive Shape Oriented Compression Technique for Low Bit Rate Images”, 3rd International Conference on Emerging Trends in Computer and Image Processing (ICETCIP'2013), Jan 8-9, 2013, Kau la lumpur, Malaysia
80. P. Srinivasa Rao & M. Madhavi Latha – “FPGA architecture for the two dimensional digital image skeletonization”, ICRAMAV-2013, Hyderabad.
81. Harshavardhini & M.Madhavi Latha – “High speed differential amplifier based comparator for future FPGA/ASIC integrated Sigma Delta ADC”, IEEE International Conference on Advanced Research Engineering and Technology (IEEE ICARET), pp. 365-369, February 8th -9th, 2013, (Print ISBN: 978-1-4673-6157-6), IEEE Xplorer Indexed, KL university A.P, India.
82. Harshavardhini & M.Madhavi Latha – “Design Development & Performance Analysis of High Speed comparator for Reconfigurable $\Sigma\Delta$ ADC with 180 nm TSMC technology”, IEEE 15th International Conference on Advanced Computing Technologies (ICACT), Sept 21-22, 2013. (Print ISBN: 978-1-4673-2816-6), IEEE Xplorer Indexed, Rajampet, A.P, India.
83. N. Alivelu Manga and Dr. M. Madhavi Latha., "An optimum ADC output word length selection for low power communication architectures," International Conference on Advances in Computing, Communications and Informatics (ICACCI), IEEE 2013, vol., no., pp. 569 - 574, 22-25 Aug. 2013, ISSN:978-1-4799-2432-5,DOI: 10.1109/ICACCI.2013.6637235.
84. V. Murali Krishna & M. Madhavi Latha - "An Optical Flow approach to robust face recognition under expression variations" Pg. No 316-322 published in International Conference on navigational Systems & Signal Processing Applications (NSSP-2013), December 13 - 14 , 2013, University college of engineering & Technology, Acharya Nagarjuna University, Nagarjuna Nagar-522510,A.P., India.

2014

85. S. Latha & M. Madhavi Latha -"Area efficient Fractional rate conversion architecture for software defined radios" in an International conference ICSCI-2014.
86. N. Alivelu Manga and Dr. M. Madhavi Latha., "Power optimized high level synthesis of Multiply-and-accumulate (MAC) based digital filter architectures under SNR constraints", International Conference on Information Communication and Embedded Systems (ICICES) – IEEE 2014. ISBN No.978-1-4799-3834-6/14/\$31.00©2014 IEEE.
87. Dattathreya, K.Suresh, M. Madhavi Latha, MK Singh, "ANN Predicting Model Based Tampered Image Reconstruction" ICRAMAV-2014, 3RD International Conference, Elsevier, JNTUH, Hyderabad, November -2014, pp.45-48.

2015

88. V. Leela Rani & M. Madhavi Latha –"Verilog Implementation of Genetic Algorithm for Minimum Leakage Vector in Input Vector Control Approach" , IEEE Conference (IEEE Explorer), SPACES 2015, March, 2015.
89. Anwar Basha Patan, M. Madhavi Latha- "Complexity Analysis of an 8 point FFT Processor for different Butterfly Structures" is presented at International conference on Electrical, Electronics and Instrumentation Engineering (ICEEI-Aug"2015), during 20-22, August, 2015, Colombo, Sri Lanka and published in the IJCEEE, Vol.3, Issue.2, August, 2015. ISSN: 2345 – 9603.
90. Anwar Basha Patan, M. Madhavi Latha-"FPGA implementation of an efficient radix-2³ FFT algorithm" is presented and published in the proceedings of National Conference on Emerging Trends in Information, Digital and Embedded Systems (NC"e-TIDES-15), on 28th Feb, 2015 at AITS, Rajampet. ISBN No: 978-1-926488-02-8. **(National)**.
91. A. Anitha, M. Madhavi Latha, Dr. M Madhavi Latha -Scalable Recursive Convolution Algorithm for the Development of Parallel FIR Filter Architectures Springer Nature Singapore Pvt Ltd. 2018-Microelectronics, Electromagnetics and Telecommunications-Proceedings of ICMEET 2017, Pbblished 2018.

Refresher Courses / Workshops / Short Term Programs Conducted: 32

1. Coordinator for a 3-Week UGC Sponsored Refresher Course on „VLSI Design“ during 13-11-02 to 2-12-02
2. Coordinator for a one-Week UGC ASC self-supported Course on „VLSI Design & Embedded Systems“ during 26-05-03 to 31-05-03
3. Coordinator for a 3-Week UGC Sponsored Refresher Course on „VLSI Design & Embedded Systems“ during October 29th – 18th November „03
4. Coordinator for a 3-day workshop on Digital Signal Processing during 13th – 15th December 2004
5. Coordinator for a 3-week refresher course on Digital Signal Processing during 17th November – 7th December 2004
6. Coordinator for a One-day workshop on “ EDA Tools for VLSI”, 27th Sep, 2006

7. Coordinator for a 5-day workshop on “ MATLAB Programming and its Applications”, Jan-Feb, 2007
8. Coordinator for a One-day workshop on “ CMOS VLSI and ASIC Design”, 10th -11th August, 2007 in collaboration with Micro wind & Ni2 designs
9. Coordinated a 3-day workshop on “Holistic systems Management”- September, 2007
10. Coordinator for Women Student Congress – 08 during 11th -12th, July 2008 under IEEE – Hyderabad Section
11. Dr. M. Madhavi Latha and Dr. D. Srinivasa Rao Coordinated for Three – day workshop on “Cellular and Wireless Technologies” during 29th - 31st July 2008 in collaboration with Motorola (Inc)International
12. Dr. M. Madhavi Latha and Dr. T. Satya Savitri Coordinated for one-day workshop on “Tanner Tools” on 10-02-09
13. Dr. M. Madhavi Latha and Mrs. V. Vijaya Laxmi organized “Mega Bloodonation Camp” at JNTUH in association with Lions Club, Banjara Hills
14. Image Processing and its Applications – 3 day workshop, March 2009
15. Short Term Course on “XILATHON” FOR b. Tech 2nd and 3rd Year Students (20th May to 30th June, 2009)
16. A Three-day workshop on “Analog Mixed Signal Design” during 19th -21st August 2009 (CVED)
17. A Two-day workshop on Low Power VLSI Design Methodologies- 2009 during 11th -12th December 2009 (CVED)
18. One-day Advanced VLSI Program on 12th March 2010 (CVED)
19. A Three-day workshop on “Signals & Signal Processing” during 28th -30th July, 2010
20. A Five-day workshop on “VLSI Design Methodology using Mentor Graphics Tools” during 9th -13th August, 2010
21. A one-day workshop on RF System Design & Testing on 21 December 2011 at JNTUH CEH
22. A Three –Day workshop on Digital Signal Processing & Programming during 27-29 January 2012 in collaboration with Physitech Electronics, Hyderabad
23. Coordinator for a 6 days workshop on “VLSI Design Using CADENCE Tools” during 26-31 March 2012.
24. A One-day workshop on Electronic Hardware Design and Implementation during May 2012 in collaboration with Physitech Electronics Pvt. Ltd.
25. A Three-Day finishing school program on VLSI Design using Cadence Tools from 9-11 August 2012.
26. A Two-day Workshop on “Antenna Simulation using ANSYS HFSS Tools”, (Under TEQIP II), May 2-3, 2014
27. A 4-Day workshop on “PERL & Python Programming” under TEQIP-II during 22-25 December, 2015.
28. A One-Day Workshop on "Advanced Programming Techniques with MATLAB" on 16th September, 2016 under TEQIP-II in collaboration with Mathworks India Pvt., Ltd., Bangalore.
29. A Two-Day workshop on "Graphical System Design with LABVIEW", 21-22 October, 2016 under TEQIP in collaboration with NI Systems India Pvt., Ltd., Bangalore & Techlabs, Hyderabad.

30. A One-Day workshop on "Research Trends in VLSI and Embedded Systems", 5-1-2017.
31. A Two-Day workshop on "Internet of Things (IOT) Using MSP430 F5529 ULP MCU" 29-30 July, 2017 in collaboration with Edgate Technologies, Bangalore.
32. FDP on "Analog IC Design (AICD) - Circuit and Layout Design Methodologies using Cadence Design Flow" 23-27 June, 2017 In collaboration with M/s. Edgate Technologies, Bangalore.
- 33.

Refresher Courses / Workshops / Short Term Programs Attended: 19

1. One-day Workshop on "MATLAB Programming and Applications to Automation and Control Systems" during October at Hyderabad organized by Cranes Software International
2. Co-ordinator/ Participated in a Two-day workshop on "CMOS VLSI AND ASIC DESIGNS" by UGC at JNTUCEH, during Aug- 2007.
3. Attended an INDO-US Collaborative program on Signal Processing during 7th - 12th July 2008 at Infosys, Mysore campus
4. Attended an INDO-US Collaborative program on VLSI Design during 14th - 19th July 2008 at Infosys, Mysore campus
5. Attended a one day Tutorial on Analog VLSI Filters & Mixed Signal Design on 12th September 08 at JNTU conducted by C-DAC, Hyderabad
6. Attended 18th Faraday memorial Lecture on "Enabling Rural India through innovation in ICT by Dr. Ashok Jhunjhunwala on Monday 22nd Sep 08 organized by IEEE Hyderabad section.
7. Attended Prepare Future program on "Embedded Software" conducted by C-DAC, Hyderabad, 02-11-2009 to 13-11-2009.
8. Attended a one-day tutorial on " Video Streaming Technologies" 04th November 2009, C-DAC-Hyderabad
9. Attended Prepare Future program on " System Design on FPGA" conducted by C-DAC, Hyderabad, 2009
10. Dr. M. Madhavi Latha – has participated in the Faculty updation Training on " Hands-on with ARM: Programming Embedded Systems" organized by C-DAC Hyderabad with JNTUH as a part of Prepare Future project, HRD Division, Department of Information Technology, GOI for the period of Two- weeks from 02-11-2009 to 13-11-2009.
11. Acted as Judge for Research Category in NI's Educators Day conducted during 16-17 October 2012 at Chennai.
12. Participated in MATLAB Academic Summit held on 3-11-2015 at Bangalore organized by Matworks India Ltd.
13. Participated in NIDays held on 19-11-2015 at Bangalore organized by National Instruments, Bangalore.
14. Participated in NIDays 2016 **Conference**, Bangalore, October 19, 2017.
15. Participated in MATLAB EXPO 2017, 27 April 2017, HICC, Hyderabad.
16. Attended in training program on "Analog IC Design Circuit and Layout Design Methodologies Using Cadence Analog Design Flow", 22-26 May, 2017.
17. Mathworks Training on "DSPs for FPGAs", 19-21 June, 2017 conducted by Mathworks Inc., at Hyderabad.
18. Mathworks Training on "Generating HDL Code from Simulink", 24-25 July, 2017 conducted by Mathworks Inc., at Hyderabad.

19. Mathworks Training on "Programming Xilinx Zynq SoCs with MATLAB and Simulink ", 26-27 July, 2017 conducted by Mathworks Inc., at Hyderabad.

Guest Lectures Delivered: 36

1. Delivered Lecture on Industry Institute Partnership Cell Workshop on VLSI Design on the topic „CPLD & FPGA Architecture“ during March 10-12, 2004 at RVR JCOP CE, Guntur
2. Delivered Lecture on „Wavelets in Image Processing“ on AICTE-ISTE Sponsored short term training program on „Remote Sensing and Satellite Image Processing“ on 5th July 2004 at CBIT, Hyderabad
3. Delivered Lecture on UGC ASC Self supported course on the topic „Application Specific ICs“ conducted during April/May, 2005
4. Delivered Lecture on „MATLAB Programming applicable to Image Processing“ on a 3-week refresher course conducted by UGC-ASC during November 2004
5. Delivered Lecture on „Wavelet Transforms applicable to Electrical Engineering“ on a 3-week refresher course conducted by UGC ASC during Aug/ Sep 2005
6. Delivered Lecture on „MATLAB Programming applicable to Image Processing“ on a 3-week refresher course conducted by UGC-ASC during November 2005
7. Delivered Lecture on a 3-day workshop on Digital Image Processing” conducted by GNITS, Hyderabad during December 2005 on „ Digital Image Enhancement in frequency Domain“
8. Acted as Expert to evaluate Projects on project exhibition conducted by DVR CE, Kandi, Hyderabad during 2005
9. Acted as Expert on Srujana – 2006 – A technical student paper presentation conducted by ABVP
10. Expert Lecture on Rectifiers and Filters at St. Peters Engg college, Hyderabad, October 2007
11. Judge for a 2-day National Level Technical Festival, CIENCIA ‘07 on 8th & 9th March, 2007 at CVR CE, Hyderabad
12. Keynote Speaker for a National Level Technical Festival, TECHNOSMEC ‘07 on 7th March, 2007 at St. Martins Engg. College, Hyderabad
13. Delivered Lecture on Digital Image Processing in a National Level workshop at Siddharth Institute of Engineering & Technology, Puttur in 2007
14. Expert member for the recruitment of staff at Mannan Institute of Science & Technology during 30th June – 1st July, 2007
15. Expert Lecture on Signals and systems at St. Martins Engg.College- Oct 2007
16. Chaired tutorial session on Over sampling ADCS at International Conference on VLSI Design & Embedded Systems (**VLSI 2008**)during 4th-8th 2008)
17. Expert lecture on Sigma-Delta ADCS and applications to ECG Monitoring Systems – 22nd January 2008
18. Chaired the session on TECHNOVISTA 2008 – A technical student paper contest conducted by Sri Venkateswara Engg. college, Suryapet on 31-01-08
19. Chaired the session on Signal Processing –I at an International conference on RF & Signal Processing (**RSPS – 2008**) on 1-2-08
20. Co-chaired the session on Signal Processing –II at an International conference on RF & Signal Processing (**RSPS – 2008**) on 2-2-08
21. Design Contest Chair in an Silver Jubilee International Conference on VLSI Design conducted during 7-11 January 2012
22. Chaired a session on 8-01-2012 for the Tutorial on Analog & Mixed Signal Design

23. Delivered expert lecture on “Fourier Transform and Sampling Techniques” in a three day workshop on DSP and applications using MATLAB at CMR College of Engg., Hyderabad
24. Delivered expert lecture on “Introduction to Wavelets and its Transforms” in a two day workshop on “Applications of Wavelets and Multi Wavelets in Image Processing” at Lords Institute of Engg. & Technology, Hyderabad during 13th - 14th August 2008
25. Chaired the session in National Level Conference on “Recent Trends in Communication Technologies and VLSI Design (RTCTV)” – organized by Vardhaman College of Engineering, Shamshabad, Hyderabad during 2-3 June 2010.
26. Chaired the session on 11th June 2010 in 1st International Conference on “Emerging Trends in Signal Processing & VLSI Design” – organized by Gurunanak Engineering College, Hyderabad during 11-13 June 2010.
27. Delivered a lecture on “ Digital Signal Processors & Architectures and programming” at CMRCET on 7-6-10
28. Expert Member and delivered lecture on “ Computational Accuracies in DSPs” in a 5-day workshop conducted by RCI, Hyderabad to train the faculty of Defense (DLRL/RCL) of various centers conducted during 9th -13th August 2010
29. Expert Member and delivered lecture on “Digital Signal Processors & Architectures” in a 5-day workshop conducted by RCI, Hyderabad to train the faculty of Defense (DLRL/RCL) of various centers conducted during 9th -13th August 2010.
30. Delivered an Invited Lecture on “Wavelet Transforms”, in 3rd IC on “Trends in Information Sciences & Computing” organized by Satyabhama University, Chennai during 8-9 December, 2011.
31. Delivered an Invited Lecture on “Wavelet Transforms and their Applications”, 2014, RCI, DRDO.
32. Delivered an Invited Lecture on “Research Methodology” at Vignan Institute of Engg., & Technology under IEEE Students Chapter during October, 2014.
33. Co-Chairman of Tutorials in PrimeAsia 2015-An International conference on Post Graduation Research in Microelectronics and Electronics, a joint conference of CAS & EDS and IEEE Hyderabad Section organized at Vasavi College of Engineering, Hyderabad during 27-29 November, 2015.
34. Chaired the session in an IC on Micro-Electronics, Electromagnetics and Telecommunications (ICMEET) - 2017 at Hyderabad organized by BVRIT Hyderabad Engineering College for Women during 9-10 September, 2017.
35. Delivered a lecture on VLSI Design Tools on 12 June, 2017 at Tirumala Engg., College, Narasaraopet, Andhra Pradesh.
36. Chaired a session in an IC on at Jagruthi Institute of Engineering and Technology, Hyderabad.

Educational Tours Organized: 05

- Conducted a to Visit Team Asia Greaves Semiconductors Pvt Ltd, Patancheru, Hyderabad for II B. Tech – ECE on 12-04-2000
- Organized a Visit to Satellite Earth Station, Shadnagar, on 28-04-2000 for UG & PG students
- Tour to North India for IV B.Tech – ECE during 2002
- Educational Visit to Satellite Earth Station, Shadnagar, during 2013 for UG & PG students

- Organized an Educational Visit to Research Centre Imarat (RCI), DRDO during 2014 for UG students

Extra Curricular Activities: 12

1. Athletic champion in Intermediate for 2 Years
2. Athletic champion in B. Tech for 4 Years
3. Winner of Chess, Carroms, Tennis in Intermediate And B. Tech Level
4. As Secretary of JNTU women Employees Association from 2001- 2003
5. As Vice – President of JNTU women Employees Association from 2003- 2007
6. Active Participant and Winner of games conducted for Employees of JNTU CE, Hyderabad
7. Active Participant of Summer camps conducted by LIONS CLUB at Pedanandipadu, Guntur (D.T), AP.
8. Organized a camp to visit to Old age homes and donation in the form of clothes, Water filter & Fans
9. Organized Blood donation camp on behalf of JNTU Women Employees Association in 2006
10. President of JNTU Women Employees Association from 7-2-08
11. Dr. M. Madhavi Latha and Mrs. V. Vijaya Laxmi organized “Mega Blood Donation Camp” at JNTUH in association with Lions Club, Banjara Hills.
12. Conducted “Lead India – 2020” program for the students of JNTUH CEH in the Department of ECE, JNTUH CEH.

PG Projects List
Guided by
Dr. M. MADHAVI LATHA, PECE, JNTUH CEH

S. No	H. T. No	Name of the Student	Title of the Project
1.	98011D0612	M. Ramana Redy	Implementation of Sampling Rate Conversion for Multistage FIR Filters and Ploy phase Networks
2.	98011D0607	D. Rama Krishna	Implementation of Codebook Search on Carmel DSP Processor
3.	98011D0601	G. N. Srinivas	Voice Activity Detector
4.	97011D0604	Dhiraj Sunera	Image Thinning using Artificial Neural Networks
5.	98011D0604	K. Saritha	Implementation, Simulation & Verification of Position Loop Compensator using 'C' Language in PC using Data Acquisition Add-on Card for Tracking RS Satellite
6.	97011D0607	K. Obulesh	Modeling of Stack Computer using VHDL
7.	99011D0605	G. Shyam Prasad	Altitude Detection using 128-pt FFT and DSP Filter – Its ASIC Implementation
8.	99011D0608	B. Subramanyeswara Rao	Simulation of Stochastic Gradient based Algorithms (ASP)
9.	00011D0616	Ch. Subramanyam	Development of S/W Algorithms for Carrier Recovery & I-Q Detection of Digitally Modulated Signals
10.	00011D0606	M. Ramakalavathi	Development of Direction Finding Algorithm
11.	00011D0619	K. Mallikarjuna	Development of FPGA based Fast Algorithms for Velocity Estimation and Pattern Recognition
12.	01011D4518	Shashi P. Deshmukh	Coded Waveforms Generator using DSP TMS320F240
13.	01011D4510	Vishnu Sankar. S	Video Segmentation Technique for Multimedia Applications
14.	01011D4501	D. Narendra	Image Coding using DWT & Implementation on DSP Processors
15.	01011D4514	L. Ram Mohan Rao	Implementation of JPEG2000 Standards o DSP Processor
16.	01011D4503	B. Chinna Rao	Noise Cancellation using Optimum Filters

S. No	H. T. No	Name of the Student	Title of the Project
17.	01011D4509	B. Meenakshudu	Transform Image Coding with Adaptive Fuzzy Systems
18.	01011D4516	G. Bal Kishan	Implementation of JPEG Codec using DCT Algorithm
19.	01011D4502 (2004)	P. Anna Purna	Speech Coding using Conjugate Structure Algebraic –nCELP
20.	01011D4504	M. Indira Chakravarthi	Digital Implementation of the 4-D Wigner Distribution Function Application to Space Invariant Processing of Real Images
21.	01011D0616	Ch. Srinivasa Rao	Design & Synthesis of High Speed RISC Microcontroller
22.	01011D0618	S. Srinivasa Rao	Multiuser Channel Estimation and Detection in W-CDMA Receiver
23.	01011D4521	T. Rama Krishna	Script Identification from Document Images using Wavelet Transform Techniques
24.	01011D0606	V. Venkatesh	Motion based foreground Segmentation
25.	01011D4520	B. Satish Chandra	Digital Audio compression by using Perceptual & Huffman Coding
26.	01011D4505	K. Venkateswara Rao	Implementation of Max. Log-Map Algorithm in Turbo codes
27.	01011D4512	Ch. Pamuleti	A Study of Wavelet Theory and its Applications to Document Image Analysis
28.	02011D4524	G. Srilakshmi	Portable Control Device
29.	01011D0607	A.Sunitha	Link Adaptation & Receiver Design for Enhanced General Packet Radio Wireless Communication
30.	02011D4510	P. Syama Sunder	Audio Signal Processing using TMS320C6711 DSP
31.	02011D0603	K. Praveen Kumar	Design of Digital Pulse Compression System for Airborne Radar
32.	02011D4505	G. Amara Jyothi	Automatic Image Registration using Genetic Algorithm
33.	02011D4514	V. Pulla Reddy	Wavelet based Image Compression using FPGA Realization
34.	01011D0604	K. Durga Prasad	Simulation of Line Path Detection and Location using Phase Path Method for FPGA

S. No	H. T. No	Name of the Student	Title of the Project
35.	01011D4523	Ch. Mahesh	Automatic Speech Recognition using HMM
36.	02011D4525	S. Syamala	Bit Plane Extraction of Multi Special Images
37.	01011D4513	S. A. Neelkanth	Design and Implementation of Image Processing Polyphase FIR Filter
38.	02011D4519	B. Tulai Soujanya	Real Time Denoising and Shaping of Audio Signals in Wavelet Domain
39.	01011D4525	V. Krishna Naik	Speech Encryption and Decryption using TMS320C5402 DSP
40.	02014D0610	R. Padmaja	Content based Image Retrieval
41.	02011D4508	N. Bhadri Prasad	Video Stabilization
42.	01011D4506	A.Rajaiah	VHDL Realization of Fast WT for Speech Processing Analysis
43.	03011D4512	D. Sneha Latha	Denoising of Images
44.	02014D0601	B. Ramesh	VLSI Design and Simulation of JPEG baseline Encoder model using VHDL
45.	03011D4516	G. Raghavaiah	Analysis and Decoding of MPEG-4
46.	03011D4504	M. N.G.V Kumar	Multiplicative Lateral Inhibition Models for Sharpening & Smoothing of Images
47.	03011D4501	A.Syedu Ibrahim	Digital Water Marking
48.	03011D45104	M. Veena Kumari	Characterization of MEMS Gyro sensor Noise using Wavelets
49.	03011D4506	J. Vishwanadh	Image Compression with Bit Map and JPEG Standards
50.	01011D4511	V. Jagadeshwara Reddy	Implementation of 4.8 Kbps Low Bit Rate Speech Coding System
51.	04011D4516	D. Saritha	Implementation of 1024-point Complex FFT on DSP Processor and FPGA
52.	01014D0619	S. Jagadeesh	Image Recognition using Fourier Melon Transform
53.	03011D0612	S. Praveena	Enhancement to Moving Image Transmission
54.	04011D4519	P. Lakshmi	Multiple Wavelet Image Denoising using Besov Ball Projection
55.	04011D4517	Sandhya Sami Reddy	Embedded Image Compression based on Wavelet Pixel Classification and Sorting
56.	03011D4523	B. Hari Krishna	Implementation of Edge Detection based on Statistical Properties
57.	04011D4502	U. N. Sateesh	Implementation of FET based Restoration with out Boundary Artifacts
58.	03011D0620	B. Vishala Raja Zakaria	Zacharias Session Initiation Protocol (VOIP-SIP)
59.	03014D0614	K. Jagadeesh Babu	Test Data Compression
60.	02011D0624	V. Amareswar	Lossy Image Compression using DCT Burrows Wheeler Transform and Arithmetic Coding

S. No	H. T. No	Name of the Student	Title of the Project
61.	04011D4512	N. Siva Prasad	Performance Analysis of Different Packet loss Concealment Algorithms for VIOP Applications and Implementing of G.711A PLC Scheme on MIPS Lexra Core
62.	02011D0614	S. Shafiulla Basha	Implementation of Morphological Image Processing Operations
63.	04011D4525	V. Mallikarjuna Reddy	Implementation of FFT based Time Delay and Doppler Acquisition
64.	04011D4503	Ch. Kesava Rao	Performance Comparison of 2 families DSPs for Image Tracking Applications
65.	04011D4513	K. Ravi Kumar	Embedded Wavelet Video Codec using 3-D SPIHT
66.	02014D0604	D. Ravi Kiran Babu	Design of Dual Channel Spread Spectrum Receiver for CDMA Applications using VHDL
67.	04011D4524	V. Srinivas	Image Compression using Block Matching Algorithm
68.	04011D4508	P. Anil Kumar	Implementation Flexible Sampling Rate Conversion on Blockfin ADSP BF533 Processor
69.	04011D4521	P. Lakshmi	Implementation of GT11 Speech Codec on ADSP 21533 Processor
70.	01014D0602	N. Srinivas Chakravarthy	Rotation Scale and Translation Invariant Digital Image Water Marking using Fourier Mellin Transform
71.	04014D0612	K. Padma Vasavi	Edge Detection using Local Thresholding Statistical Approach
72.	04014D0607	V. Leela Rani	H/W Implementation of AEL Algorithm
73.	05011D4507	V. Sudheer Kumar	Biometric Security using Finger Print Verification
74.	05011D4514	Ch. Jayakar	Enhancement of Fire Modules
75.	05011D4510	K. Arun Kumar	VLSI Implementation of PC Balanced aB/10B Endec
76.	05011D4503	A.Prasanth	Image Compression using Multi Wavelets
77.	04012D0608	Syed Karim Saheb	Implementation of VME Bus Slave Controller
78.	04012D0613	P. Sreenivasa Rao	Implementation of Internet Low Bit Rate CODEC Algorithm for VOIP Applications
79.	04012D0605	S. Latha	VLSI Implementation of PI/4 QPSK
80.	04012D0614	N. Alivelu Manga	FPGA Implementation of Direct Digital Frequency Synthesizer based on CORDIC Algorithm

S. No	H. T. No	Name of the Student	Title of the Project
81.	05012D0613	B. Satish	High Throughput and Low Power Implementation of AES Algorithm using Cadence Tools
82.	05012D0630	R. Bramaramba	Implementation of Pipelined Aray Based FIR Filter Folding using Verilog HDL
83.	05012D0614	S. Srividya	A Fast Wavelet Deconvolution and Fourier Wavelet Regularized Deconvolution for Image Deblurring
84.	04012D0609	B.S.Prasada Rao	Implementation of 32-bit Floating Point RISC Processor using VHDL
85.	05012D0627	P. Surya Kumari	A Low Bit Rate DSP based Modulator using High Performance 32-bit DSP
86.	04012D0601	T. Santish Kumar	Hardware Implementation of FEC in Spartan-3E FPGA for Advanced Ground Radio
87.	04012D0604	K. Yakaiah	Implementation of FPGA based Radar Signal Simulator
88.	04012D0615	K. Panduranga Chary	Session Initiation Protocol based Soft Phone
89.	04012D0610	P. Jayasimha Reddy	Real time Streaming Protocol based Mobile TV
90.	05012D0607	B. Sridevi	Lossless Steganography for AMBTC Compressed Images
91.	05012D0629	P. Venkatapathi	FPGA Implementation of Extended Tiny Encryption Algorithm (XTEA) using VHDL
92.	06012D0629	D. Hari Babu	FPGA Implementation of Direct Digital Frequency Synthesizer
93.	06012D0603	P. Haneef Saheb	ASIC Implementation of Hardware Grid Routing Accelerator
94.	06012D0612	A.Anitha	A Multistage Motion Estimation Scheme for Video Compression
95.	06012D0614	B. V. Rajani	Audio Denoising by Time Frequency Block hresholding
96.	06012D0607	B. Niraimathi	ESM Simulator
97.	06012D0625	K. Mamatha	FPGA Implementation of STBC Decoder
98.	06012D0610	S. Rajani Kumari	Simulation of ireless Sensor System using HDL

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99.	05012D0603	P. Pragathi	Implementation of ISP with RTOS based for Industrial Automation
100.	06012D06179	P. Satyanarayana Goud	Neighshrink Window based Double Haar Wavelet Transform for Image Processing
101.	04012D0622	J. Srinivas	Reconstruction Algorithms for Remote Sensing Image Enhancement
102.	05012D0605	P. Nalini	Color Histogram feature based Image Classification in Content based Image Retrieval Systems
103.	07012D4530	G. Jyothi	Image Segmentation using Information Bottleneck Method
104.	04012D0611	M. Kailachandra	Target Tracking Systems using GPS Module
105.	04012D0611	Kailachandra Mahapatra	Target Tracking System using GPS Modula
106.	08012D04521	D. Subhashini	Lossless Steganography based on AMBTC Compressed Images
107.	08012D04516	G. Sahitya	32-phase Code design using MSAA
108.	09012D4515	N. Saritha	Object based Video Retrieval using Scale Invariant Feature Transform algorithm
109.	03014D0618	S. Balaiah	DSCE
110.	05014D0603	P. Srinivasulu	Face Recognition using Canny, Sobel, Prewitt Edge Detection Algorithms and Principle components analysis
111.	05014D0618	K. H. Murali	Implementation of Data Encryption for Bluetooth security using VHDL
112.	06014D0614	M. Suman	Linear Prediction coding of speech signals using Multistage Vector Quantization
113.	05014D0609	R. Prasanth	Performance Evaluation & Simulation of Routing Protocols in Ad Hoc N/Ws
114.	03014D0607	D.Nagendra Prasanna	Data Compression using BWT & Arithmetic Coding
115.	04014D0618	M. Vijaya Laxmi	Morphological Text Localization using DWT
116.	06014D609	B. V. S. L. Bharathi	Low Power Programmable Embedded Controller (LP2EC) Design
117.	06014D0625	N. Vijaya Sankar	Lossless Compression of Color Map Image by Context Tree Modeling
118.	01014D0615	G. Mallikarjun	Integrated Approach in the Detection of Vacant Parking Space
119.	07014D4505	B. Swapna	Content Based Image Retrieval
120.	07014D4522	G. Indira Priyadarshini	Implementation of Communication Coprocessor using Wormhole Routing
121.	07014D4516	R. Leelavathi	Reversible Data Hiding based on Histogram Modification using Pixel Differences
122.	07014D4501	B. Malleswari	FPGA Implementation of RS (235, 239) Encoder and Decoder
123.	06014D0619	Sk. Khadar Basha	Eye Controller Computer Interaction
124.	04012D0613	P. Srinivasa Rao	Implementation of Internet Low Bit Rate CODEC algorithm for VOIP Applications

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125.	05011D4507	V. Sudheer Kumar	Biometric Security using Finger Print Verification
126.	05011D4514	Ch. Jayakar	Enhancement for the fire modules
127.	05011D4503	A.Prasanthi	Image Compression using Multi wavelets
128.	03011D0606	Ch. Padmaja	Image Coding Techniques – A Comparative Analysis
129.	02011D0622	K. Rama Swamy	Image Deniosing using Spatial Filtering Techniques
130.	06011D4519	V. Hari Priya	Reconstruction of Rx3i Ethernet module to comply with the direction of restriction on Hazardous substances
131.	06011D4520	M. Goutham Aro	Implementation of 2x1 Space Time coding
132.	06011D4513	S. Dhana laxmi	Low Bit Rate Satellite Demodulator for Brust mode
133.	06011D4507	B. Pradeep Naik	Digital Design of TMS320LF2403A based board for BLDC Motor Control
134.	06011D4518	G. Appa Rao	Programmable Digital Frequency Synthesizer
135.	07011D4510	D. Anitha Rani	Impleentation of Industrial Communication Protocols
136.	07011D4511	A.Shanker	Alias Free Sub-band Adaptive Filtering with Critical Sampling
137.	07011D4517	B.Pavan Kumar	Post Processing Low Bit Rate Block DCT coded Images based on Fields of Expert Prior
138.	07011D4502	G. Janardhan Reddy	Dual Microphone Speech Enhancement
139.	07011D4509	B. Varahala Babu	Compression Domain Color Image Enhancement
140.	04011D4504	K. Raju	Robust Linear Prediction Analysis for Low Bit Rate Speech Coding
141.	07011D4501	M.V.S.S. Prasad	Estimation of Instantaneous Pitch of the Speech Signal
142.	07014D4505	B. Swapna	Content Base Image Retrieval
143.	08011D0623	N. Kameswara Rao	Low Power VLSI Architecture for Lifting based
144.	08011D4517	J. Amarendra	Adaptive Noise Cancellation using modified Dynamic Fuzzy Neural N/Ws
145.	08011D0613	M. Rajesh	High Performance Low Power and Optimum area 2-D DCT architecture
146.	08011D4509	Ch. Babji Prasad	Fast LMS Algorithm for Stereophonic Acoustic Echo Cancellation

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147.	07011D4504	S. Eswar Kumar	ECG Feature Extraction using Multi resolution Wavelet Transform
148.	08011M0609	Paidi Ganag Mohan	Image Processing via Bessel Transformation
149.	09011D4521	Mazaiyar Khosravi	Face Detection based Rejection Classification in MATLAB
150.	09011D0625	Mohammad Golabi D	An Effective Approach of Clustering for abnormal MR Brain Image Segmentation
151.	09011D0611	J. Neelima	A New VLSI Architecture of Parallel Multiplier Accumulator based on Modified Booth Algorithm
152.	09011D0601	S. Khader Bhasha	Design and Implementation of 64-bit RISC Processor using VHDL
153.	09011D5502	A.Archana	Developing Firmware for Industrial HMI Panel
154.	09011D0606	P. Santhosh	FPGA based Advanced Sowing and Planting Equipment Controller Design
155.	09011D0625		
156.	10011D4518	D. Sudheer	Moving Target Detection in Video Surveillance
157.	10011D0612	S. Vijaya Kumar	Cryptography and Steganography for Audio Applications
158.	10011D0606	Ramesh Dandu	Satellite Image Resolution Enhancement
159.	10011D4514	Sesha Sai. M	Determining Human Entity in Video Surveillance
160.	05012D0613	B. Satish	High Throughput and Low Power Implementation of AES Algorithm using Cadence Tools
161.	05012D0630	R. Bramaramba	Implementation of Pipelined Aray Based FIR Filter Folding using Verilog HDL
162.	05012D0614	S. Srividya	A Fast Wavelet Deconvolution and Fourier Wavelet Regularized Deconvolution for Image Deblurring
163.	04012D0609	B.S.Prasada Rao	Implementation of 32-bit Floating Point RISC Processor using VHDL
164.	05012D0627	P. Surya Kumari	A Low Bit Rate DSP based Modulator using High Performance 32-bit DSP
165.	04012D0601	T. Santish Kumar	Hardware Implementation of FEC in Spartan-3E FPGA for Advanced Ground Radio
166.	04012D0604	K. Yakaiah	Implementation of FPGA based Radar Signal Simulator
167.	04012D0615	K. Panduranga Chary	Session Initiation Protocol based Soft Phone

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169.	05012D0607	B. Sridevi	Lossless Steganography for AMBTC Compressed Images
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172.	06012D0603	P. Haneef Saheb	ASIC Implementation of Hardware Grid Routing Accelerator
173.	06012D0612	A.Anitha	A Multistage Motion Estimation Scheme for Video Compression
174.	06012D0614	B. V. Rajani	Audio Denoising by Time Frequency Block hresholding
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177.	06012D0610	S. Rajani Kumari	Simulation of ireless Sensor System using HDL
178.	05012D0603	P. Pragathi	Implementation of ISP with RTOS based for Industrial Automation
179.	06012D06179	P. Satyanarayana Goud	Neighshrink Window based Double Haar Wavelet Transform for Image Processing
180.	04012D0622	J. Srinivas	Reconstruction Algorithms for Remote Sensing Image Enhancement
181.	05012D0605	P. Nalini	Color Histogram feature based Image Classification in Content based Image Retrieval Systems
182.	07012D4530	G. Jyothi	Image Segmentation using Information Bottleneck Method
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187.	09012D4515	N. Saritha	Object based Video Retrieval using Scale Invariant Feature Transform algorithm

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188.	11011D4514	B. Rajender Reddy	Partial Adaptive block based super resolution method.
189.	11011D4510	D. Praveen Kumar	Video Analytics using background subtraction and object tracking
190.	11011D4503	T. Rama Krishna	Implementation of Data embedding methods using optimal pixel adjustment process and adaptive pixel pair matching.
191.	11011D0613	D. Raghavender	Design of energy efficient logic circuits using adiabatic technique
192.	11011D0628	M. Sadhu Sunder Singh	Design of optimal adder for low power and high speed applications
193.	12011D0624	K. Pramod Kumar Reddy	Leakage reduced low power VLSI design using Lector, Galeor and Stacked sleepy techniques
194.	12011D4519	V. Bhanu sai prakash	Improved accuracy, specificity, sensitivity in segmentation of tissues in MR Images using modified spatial fuzzy C-means algorithm.
195.	12011D4516	A. Prasad	Secret key based QR image video watermarking scheme for copyright protection
196.	12011D4512	Sailesh Gajwel	Steganography using wavelet families and statistical methods
197.	11012D4507	S. Ravi Kumar	Dualtree complex wavelet transform based image denoising through partial difference
198.	11012D4503	A. M. Sai Sunetha	DCT Statistics model based Blind Image quality Assessment
199.	10012D4507	B. Ravindra Naidu	Implementation of audio denoising using time frequency block threshold
200.	10012D4503	G.Joy	Portal Radiation Acquisition monitoring System
201.	10012D425	E. Rani Priscilla	Enhancement of Universal Demosaicing Algorithm
202.	09012D4528	N. Kavitha	Nonlinear Transform for reduction in PAPR & Impact of Timing Jitter & I/Q imbalance in OFDM
203.	13011D4501	G.V. Bharath	A Secure image transmission technique using reversible color transformations.
204.	13011D4502	V. Ram Chandra Reddy	Robust image dehazing and matching based on transmission MAP estimation and sift descriptor.
205.	13011D4508	D. Sandeep	An effective video watermarking using color histogram analysis and bit-plane image arrays.

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206.	13011D4510	Y. Rangaswamy	Aircraft recognition in high resolution satellite images.
207.	10012D4513	Babu RamYadav	Performance Evaluation of Direction of Arrival (DoA) Estimation using MATLAB
208.	12012D4502	Bhavana Gowlikar	A Novel Scheme for Rejection of Narrow Band GPS Jamming
209.	14011D0619	M. Santhoshi	Image Watermarking using Mojette Transform
210.	14011D0607	M. Guravaiah	Design and Analysis of Bus Bridge interface between AXI & OCP protocols using Verilog HDL
211.	14011D0603	G. Ram Prasad	Design of UART with BIST capability
212.	14011D0602	Ch. Naresh	Design and Implementation of DSITC on FPGA
213.	13012D4501	K. Chakradhar	Implementation of efficient delayed least mean square adaptive filter and its application
214.	12012D4523	K. Kurma Rao	Analysis and Design of digital Servo loops for Antenna control
215.	12012D4507	Vedriithi Tiwari	Binarization of degraded document using adaptive image control
216.	13012D4521	Md. Raunaque Hasan	Novel based LS and MMSE Channel Estimation Techniques for OFDM Systems
217.	12011P0403	H. Ayesha Anjum	Data Transmission using Software Defined Radio