



Dr. M. Madhavi Latha
Director - Innovative Technologies
Professor of ECE & BOS Chairperson-BME/EIE/ICE/ECM, JNTUH

Dr. M. MadhaviLatha obtained B. E (NU) in 1986, M. Tech (JNTU) in 1993 and the first internal Ph. D (JNTU) in 2002. She has more than 3 years of teaching experience in C. R. Polytechnic College, 4 years of Industrial experience in ECIL, Power Electronics, Midfield Steels Ltd., 21 years of teaching experience in JNTUH since 1994, where she is currently working as Professor of ECE & Director-Innovative Technologies, JNTUH. She established the first Digital Signal Processing Laboratory under AICTE- TAPTEC Project & also in support of Texas Instruments, Austin, USA and Analog Devices, USA. She has good rapport with Ni2 Designs, Cadence Design systems, Mentor Graphics, Synopsys Intl., Xilinx, Trident Tech Labs, VEDA IIT, AMD industries and NRSA, CDAC, DLRL and RCI organizations. She established the first Center for

Excellence in VLSI & Embedded Systems Design (CVED) in ECE department and is the coordinator of CVED. She published **175** papers in National / International Journals / Conferences held at USA, Austria, Greece, Singapore and IEEE region-9 conference at Russia. She coordinated 20 UGC/JNTUH refresher courses / workshops in VLSI, Signal / Image Processing areas and community services. She guided **16** scholars, **4** scholars submitted their Ph. D work and guiding **4** scholars for Ph. D in the area of Signal Processing, Mixed-Signal, Low Power VLSI, Speech / Image Processing. She guided **208** PG projects. She worked as the first woman Officer in-charge, Examination branch, JNTUH for about 3 ½ years. She worked as Professor In-charge Library, JNTUH from 2008-2010. She worked for JNTU Women Employees Association in various categories up to President of JNTUH WEA. She held the position of First Chairman for Women in Engineering (WIE) society – IEEE Hyderabad section, conducted Women Student Congress-2008 and many other activities and received the appreciation certificate from IEEE, USA. She was the Member, Board of Studies for EIE / BME / ETM /ICE group board, is the member, Board of studies for ECE/ETM groups and proposed MATLAB simulation, DSP laboratory and ECAD & VLSI laboratories & many subjects. She was the member of AICTE Inspection Committee and is one of the Chairmen of JNTUH FFCs; Governing Body Member of several JNTUH affiliated colleges. She was the visiting faculty at IIIT, Hyderabad during 2002-03 years. She is an expert member of selection committee for UGC, UPSC, APPSC, DRDO, NRSC, KU/ICFAI University and other affiliated colleges, Ph. D adjudicator for PSG, Anna University, Satyabhama Deemed University, SatyaSai Deemed University and Chandigarh University. She was an expert member of various conferences viz. SPPRA – 2009, Austria, ICRSPS-2008, A. P, Board of Studies member of JNTUH, JNTUK, Satyabhama Deemed University, Vignan University, KLU and Academic council member of KL University. She is the key person in establishing the MOU with Cadence Design Systems, Mentor Graphics, Bangalore, and Synopsys Inc., USA, brought complete suite of Cadence Tools (for 20 users, worth of Rs.20 Lakhs) & Mentor Graphics Tools

(for 20 users, worth of Rs. 20 Lakhs), Synopsys Tools (for 50 users, worth of \$ 250M and SEER Akademi as part of activities of CVED). She has visited 10 European countries including Sweden as the Coordinator of M. Tech-DDP program with BTH, Sweden, and Singapore. She served as the Design Contest Chair for VLSI Conference 2012 held during 7-11 January 2012. She received AICTE-MODROBS project for Rs. 8-00 Lakhs for Communication Laboratory during 2012. She completed successfully Headship in September 2012 and established Basic Electronics laboratory under UGC XI plan, Seminar Hall and improved the infrastructure in all the laboratories. The Department received DST- FIST project for Rs. 55-00 Lakhs in November 2012 under her leadership as coordinator FIST-2012. She visited Univ. of California, Berkeley, San Jose University and Stanford University for research collaborative activities in September 2012. She is the recipient of **Best Teacher Award-2013** by Andhra Pradesh State Government. She visited Bosch Rexroth, Germany and Blekinge Institute of Technology and Linköping University, Sweden as part of MOU and research collaborative activities during October 2-12, 2013. She conducted successfully **EAMCET-2014** as Co-Convener and AP Forest Department Recruitment Test (**AP FDRT**) -2014 as Convener. She is the Convener of e-Learning Solutions and Two-Way HD Delivery Mechanism for Teachers & Students (e-LSDM) project at JNTUH and got the best appreciation from NPTEL Coordinator, MHRD, New Delhi. She was selected by Department of State, Bureau of Educational and Cultural Affairs (ECA) and the Institute of International Education (IIE) for EducationUSA Leadership Institute: Industry/Private Sector Linkages & Innovation during 10-23 February, 2015.

Tutorial Co-chair in PrimeAsia (Asia-Pacific Conference on Postgraduate Research in Microelectronics & Electronics) 2015 being organized by Vasavi College of Engineering, Hyderabad, Telangana.

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