

**JNTUH COLLEGE OF ENGINEERING (Autonomous)**  
**EXAMINATIONS BRANCH, HYDERABAD - 85**

**The following is the provisional list of successful candidates appeared for  
IV B.Tech I Sem.(Supply) IIDMPP.(R09)examination held in April,2018**

-----  
Branch : CIVIL ENGINEERING  
-----

Passed in All Subjects :

----- NIL -----

.....  
Passed in Subject: (ID101) THEORY OF ELASTICITY & PLASTICITY

----- NIL -----

.....  
Passed in Subject: (ID102) THEORY OF PLATES

----- NIL -----

.....  
Passed in Subject: (ID103) ADVANCED STRUCTURAL ANALYSIS

----- NIL -----

.....  
Passed in Subject: (ID104) PROJECT MANAGEMENT SYSTEMS

----- NIL -----

.....  
Passed in Subject: (ID105) CONSTRUCTION MANAGEMENT

----- NIL -----

.....  
Passed in Subject: (ID106) ADVANCED STRUCTURAL ANALYSIS

----- NIL -----

.....  
Passed in Subject: (ID151) P.G.-(LAB)

----- NIL -----

.....  
Passed in Subject: (ID191) SEMINAR

----- NIL -----

.....  
Passed in Subject: (ID192) COMPREHENSIVE VIVA

----- NIL -----

.....  
Passed in Subject: (ID193) U.G. PROJECT

----- NIL -----

.....  
**\*\* 12011M3222 MALPRACTICE IN SUBJECT ID105 -> CONSTRUCTION MANAGEMENT**

Date:22-05-2018

Sd/-  
PRINCIPAL

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**The following is the provisional list of successful candidates appeared for  
IV B.Tech I Sem.(Supply) IIDDMP.(R09)examination held in April,2018**

-----  
Branch : ELECTRICAL & ELECTRONICS ENGG.  
-----

Passed in All Subjects :

----- NIL -----

.....  
Passed in Subject: (ID201) REACTIVE POWER MANAGEMENT

----- NIL -----

.....  
Passed in Subject: (ID202) POWER ELECTRONIC CONTROL OF D.C.DRIVES

----- NIL -----

.....  
Passed in Subject: (ID203) POWER ELECTRONIC CONVERTERS

----- NIL -----

.....  
Passed in Subject: (ID204) VLSI DESIGN

----- NIL -----

.....  
Passed in Subject: (ID205) INDUSTRIAL ROBOTICS

----- NIL -----

.....  
Passed in Subject: (ID206) MODERN CONTROL THEORY

----- NIL -----

.....  
Passed in Subject: (ID251) P.G.-(LAB)

----- NIL -----

.....  
Passed in Subject: (ID291) SEMINAR

----- NIL -----

.....  
Passed in Subject: (ID292) COMPREHENSIVE VIVA

----- NIL -----

.....  
Passed in Subject: (ID293) U.G. PROJECT

----- NIL -----  
.....

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-----  
Branch : ELECTRONICS & COMMUNICATIONS ENGG.  
-----

Passed in All Subjects :

----- NIL -----

.....  
Passed in Subject: (ID401) VLSI TECHNOLOGY & DESIGN

----- NIL -----

.....  
Passed in Subject: (ID402) NETWORK SECURITY & CRYPTOGRAPHY

----- NIL -----

.....  
Passed in Subject: (ID403) TCP/IP & ATM NETWORKS

----- NIL -----

.....  
Passed in Subject: (ID404) SYSTEM ON CHIP ARCHITECTURE

12011M3405

.....  
Passed in Subject: (ID405) WEB TECHNOLOGY

----- NIL -----

.....  
Passed in Subject: (ID406) MICROCONTROLLERS FOR EMBD.SYS.DESIGN

----- NIL -----

.....  
Passed in Subject: (ID451) P.G.-(LAB)

----- NIL -----

.....  
Passed in Subject: (ID491) SEMINAR

----- NIL -----

.....  
Passed in Subject: (ID492) COMPREHENSIVE VIVA

----- NIL -----

.....  
Passed in Subject: (ID493) U.G. PROJECT

----- NIL -----

.....

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-----  
Branch : COMPUTER SCIENCE ENGINEERING  
-----

Passed in All Subjects :

----- NIL -----

.....  
Passed in Subject: (ID501) WEB SERVICES & SERVICE ORIENTED ARCH.

----- NIL -----

.....  
Passed in Subject: (ID502) NEURAL NETWORKS

----- NIL -----

.....  
Passed in Subject: (ID503) ADVANCED ALGORITHMS

12011M3501

.....  
Passed in Subject: (ID551) P.G.-(LAB)

----- NIL -----

.....  
Passed in Subject: (ID591) SEMINAR

----- NIL -----

.....  
Passed in Subject: (ID592) COMPREHENSIVE VIVA

----- NIL -----

.....  
Passed in Subject: (ID593) U.G. PROJECT

----- NIL -----  
.....

Date:22-05-2018

Sd/-  
PRINCIPAL