

**JNTUH COLLEGE OF ENGINEERING (Autonomous)**  
**EXAMINATIONS BRANCH, HYDERABAD - 85**

**The following is the provisional list of successful candidates appeared for**  
**IV B.Tech I Sem.(Supply) IIDDMP.(R13)examination held in November,2019**

-----  
Branch : CIVIL ENGINEERING  
-----

Passed in All Subjects :

----- NIL -----

.....  
Passed in Subject: (ID105) CONSTRUCTION MANAGEMENT

----- NIL -----

.....  
Passed in Subject: (ID107) COMPUTER ORIENTED NUMERICAL METHODS

----- NIL -----

.....  
Passed in Subject: (ID108) PROJECT PLANNING & FINANCIAL MANAGEMENT

----- NIL -----

.....  
Passed in Subject: (ID109) PERFORMANCE MANAGEMENT

----- NIL -----

.....  
Passed in Subject: (ID152) ADVANCED INFRASTRUCTURE-(LAB)

----- NIL -----

.....  
Passed in Subject: (ID153) GIS & ADVANCE SURVEYING -(LAB)

----- NIL -----

.....  
Passed in Subject: (ID192) COMPREHENSIVE VIVA

----- NIL -----

.....  
Passed in Subject: (ID193) U.G. PROJECT

----- NIL -----

.....

Date:27-12-2019

Sd/-  
PRINCIPAL

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**The following is the provisional list of successful candidates appeared for**  
**IV B.Tech I Sem.(Supply) IIDDMP.(R13)examination held in November,2019**

-----  
Branch : ELECTRICAL & ELECTRONICS ENGG.  
-----

Passed in All Subjects :

----- NIL -----

.....  
Passed in Subject: (ID201) REACTIVE POWER MANAGEMENT

----- NIL -----

.....  
Passed in Subject: (ID203) POWER ELECTRONIC CONVERTERS-I

----- NIL -----

.....  
Passed in Subject: (ID207) DIGITAL SIGNAL PROCESSING

----- NIL -----

.....  
Passed in Subject: (ID208) HVDC TRANSMISSION

----- NIL -----

.....  
Passed in Subject: (ID209) ARTIFICIAL NEURAL NETWORKS & FUZZY SYS.

----- NIL -----

.....  
Passed in Subject: (ID252) DIGITAL SIGNAL PROCESSING-(LAB)

----- NIL -----

.....  
Passed in Subject: (ID292) COMPREHENSIVE VIVA

----- NIL -----

.....  
Passed in Subject: (ID293) U.G. PROJECT

----- NIL -----

.....

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-----  
Branch : ELECTRONICS & COMMUNICATIONS ENGG.  
-----

Passed in All Subjects :

----- NIL -----

.....  
Passed in Subject: (ID401) VLSI TECHNOLOGY & DESIGN

----- NIL -----

.....  
Passed in Subject: (ID402) NETWORK SECURITY & CRYPTOGRAPHY

----- NIL -----

.....  
Passed in Subject: (ID403) TCP/IP & ATM NETWORKS

----- NIL -----

.....  
Passed in Subject: (ID404) SYSTEM ON CHIP ARCHITECTURE

----- NIL -----

.....  
Passed in Subject: (ID405) WEB TECHNOLOGY

----- NIL -----

.....  
Passed in Subject: (ID407) TRANSFORM TECHNIQUES

----- NIL -----

.....  
Passed in Subject: (ID408) INTERNETWORKING WITH TCP/IP

13011M3452

.....  
Passed in Subject: (ID452) EMBEDDED SYSTEMS DESIGN-(LAB)

----- NIL -----

.....  
Passed in Subject: (ID453) NETWORKS-(LAB)

----- NIL -----

.....  
Passed in Subject: (ID454) WEB TECHNOLOGY-(LAB)

----- NIL -----

.....  
Passed in Subject: (ID492) COMPREHENSIVE VIVA

----- NIL -----

.....  
Passed in Subject: (ID493) U.G. PROJECT

----- NIL -----  
.....

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IV B.Tech I Sem.(Supply) IIDDMP.(R13)examination held in November,2019

-----  
Branch : COMPUTER SCIENCE ENGINEERING  
-----

Passed in All Subjects :

----- NIL -----

.....  
Passed in Subject: (ID503) ADVANCED ALGORITHMS

14011M3514

.....  
Passed in Subject: (ID504) PARALLEL & DISTRIBUTED SYSTEMS

13011M3523

.....  
Passed in Subject: (ID505) SOFTWARE ARCH.& DESIGN PATTERNS

----- NIL -----

.....  
Passed in Subject: (ID552) ADVANCED ALGORITHMS-(LAB)

----- NIL -----

.....  
Passed in Subject: (ID592) COMPREHENSIVE VIVA

----- NIL -----

.....  
Passed in Subject: (ID593) U.G. PROJECT

----- NIL -----

.....  
**\*\* 13011M3521 MALPRACTICE IN PARALLEL & DISTRIBUTED SYSTEMS**

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