

**JNTUH College of Engineering Hyderabad
(Autonomous)**

B.Tech. IV year I Semester Supplementary Examinations, March-2021

(For IIDDMP Courses Only)

Examination Time Table

Exam Time: 02.00 PM to 05.00 PM

Date	EEE	ECE
1/3/2021	VLSI Design	Network Security and Cryptography
03-03-201	-	Microcontrollers for Embedded Systems Design
05-03-201	-	System on Chip Architecture
8/3/2021	-	VLSI Technology & Design

Date: 19-02-2021

**Sd/-
PRINCIPAL**