

**JNTUH College of Engineering Hyderabad
(Autonomous)**

B.Tech. IV year I Semester Supplementary Examinations, February -2022

(For IDP (B.Tech +M.Tech) Courses)

Examination Time Table

Exam Time: 02.00 PM to 05.00 PM

Date	EEE
15-02-2022	VLSI Design

Date: 31-01-2022

**Sd/-
PRINCIPAL**