

COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
M. Tech (DT) – SSP - I YEAR – I SEMESTER – 2017-18

TIME TABLE

Room No: G4

w.e.f: 04-09-2017

DAY/TIME	9.40A.M-11.20A.M	11.20A.M – 1.00P.M	2.00P.M- 3.40 P.M	3.40P.M-4.30 P.M
MON	BMSP	TT	VLSITD	
TUE	RPQT	TCP/IP	ADSP	
WED	VLSITD	TT	SP LAB	
THURS	RPQT	BMSP	ADSP	
FRI	Seminar		TCP/IP	

Subjects:

RPQT: Random Processes and Queuing Theory

ADSP: Advanced Digital Signal Processing:

TT : Transform Techniques

VLSIT&D: VLSI Technology & Design:

BMSP: Biomedical Signal Processing

TCP/IP: TCP/IP and ATM Networks

SP Lab: Signal Processing Lab

Seminar:

Faculty:

Dr. B. N. Bhandari

Dr. M. Madhavi Latha

Dr. T. Satya Savithri

Mr. M. Sampath

Mrs. A. Rajani

Dr. D. Sreenivasa Rao

Mrs. A. Rajani /

Mr. M. Kanaka Chary

Mrs. A. Rajani /

Mr. M. Rajesh

Sallys
H.E.C.E.D
5/8/17

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS AND COMMUNICATIONS ENGINEERING
M. Tech (DT) – DSCE - I YEAR – I SEMESTER -2017-18

TIME TABLE

Room No: S15

w.e.f: 04- 09-2017

DAY/TIME	9.40A.M-11.20A.M	11.20A.M – 1.00P.M	2.00P.M- 3.40 P.M	3.40P.M-4.30 P.M
MON	ERTOS	DCS	VLSITD	
TUE	ESD	TCP/IP	Seminar	
WED	VLSITD	ESD	DSD	
THURS	DSD LAB		ERTOS	
FRI	DSD	DCS	TCP/IP	

Subjects:

DSD: Digital System Design with PLDs
VLSIT&D: VLSI Technology & Design
ESD: Embedded System Design
TCP/IP: TCP/IP and ATM Networks
DCS : Digital Control Systems:
ERTOS: Embedded Real Time Operating Systems
Digital System Design Lab:

Seminar:

Faculty:

Mr. A. Shravan Kumar
Mr. M. Sampath
Dr. M. Asha Rani
Dr. D. Sreenivasa Rao
Dept. of EEE
Dr. L. Pratap Reddy
Dr. M. Madhavi Latha/
Mr. P. Shravan Kumar
Mrs. A. Rajani /
Mr. M. Kanaka Chary

Sallyt
H.E.C.E.D
34/8/17

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
M. Tech (DT) – Embedded Systems - I YEAR – I SEMESTER-2017-18

TIME TABLE

Room No: F9

w.e.f: 04- 09-2017

DAY/TIME	9.40A.M-11.20A.M	11.20A.M – 1.00P.M	2.00P.M- 3.40 P.M	3.40P.M-4.30 P.M
MON	RTOS	DCS	ES LAB	
TUE	ESD	VLSITD	ELE-III	
WED	VLSITD	ESD	DSD	
THURS		ELE-III	RTOS	
FRI	DSD	DCS	Seminar	

Subjects:

ESD: Embedded System Design:

DCS : Digital Control Systems:

DSD: Digital System Design with PLDs

VLSIT&D: VLSI Technology & Design:

Elective-III:

RTOS: Embedded Real Time Operating Systems

Embedded System Lab:

Seminar:

Faculty:

Dr. M. Asha Rani

Dept. of EEE

Mr. A. Shraavan Kumar

Ms. Ashwini

Dept. of EEE

Dr. L. Pratap Reddy

Dr. L. Pratap Reddy/

Mrs. J. Samrajyam

Ms. B. Manasa /

Ms. G. Sony

Sallys
H.E.C.E.D
31/8/17