

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
I M. Tech (SSP) I SEMESTER – 2022-23

TIME TABLE

Room No: G-5

w.e.f: 24.10.2022

TIME/ DAY	10.00am-11.30am	11.30am-1.00pm	2.00pm- 3.30pm	3.30Ppm- 4.00pm	4.00pm- 5.00pm
MON		RPQT	IPT	TT	
TUE	ADSP LAB (10.00AM-12.00PM)		SIP Lab		
WED	RPQT	ADSP	RM & IPR		
THU	ADSP	TT	IPT		
FRI	SIP Lab (10.00AM-12.00PM)		ADSP LAB		
SAT			Audit Course-1		

Subjects: Faculty

TT: Transform Techniques: Dr. T. Satya Savithri

ADSP: Advanced Digital Signal Processing: Dr. M. Madhavi Latha

RPQT: Random Processes and Queuing Theory: Dr. B. N. Bhandari

IPT: Internet Protocols and Technologies: Dr. D. Sreenivasa Rao

ADSP Lab: Advanced Digital Signal Processing Lab: Dr. M. Madhavi Latha/Mr. M. Rajesh

SIP Lab: Speech and Image Processing Lab: Dr. K. Anitha Sheela/Dr. A. Rajani

RM & IPR: Research Methodology and IPR: Dr. L. Pratap Reddy

AC-1: English for Research Paper Writing: Dr. P. Dasharatham


HECED

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I M. Tech (DSCE) I SEMESTER – 2022-23

TIME TABLE

Room No: S-15

w.e.f: 24.10.2022

TIME/ DAY	10.00am-11.30am	11.30am- 1.00pm	2.00pm-3.30pm	3.30Ppm- 4.00pm	4.00pm- 5.00pm
MON	ES LAB (10.00AM-11.00AM)	ESD	IPT		
TUE		ESD	DSD LAB		
WED	DSD	ACA	RM & IPR		
THU	ES LAB		IPT	DSD	
FRI	DSD LAB (10.00AM-12.00PM)		ACA		
SAT			Audit Course-1		

Subjects: Faculty

DSD: Digital Systems Design: Mr. A. Shravan Kumar

ESD: Embedded System Design: Dr. M. Asha Rani

ACA: Advanced Computer Architecture: Dr. M. Asha Rani

IPT: Internet Protocols and Technologies: Dr. D. Sreenivasa Rao

DSD Lab: Digital Systems Design Lab: Dr. T. Satya Savithri/ Mr. P. Shravan Kumar

ES Lab: Embedded System Lab: Mr. A. Shravan Kumar /Mr. C. Vinay Kumar (Guest Faculty)

RM & IPR: Research Methodology and IPR: Dr. L. Pratap Reddy

AC-1: English for Research Paper Writing: Dr. P. Dasharatham



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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
I M. Tech (ES) I SEMESTER – 2022-23

TIME TABLE

Room No: T-7

w.e.f: 24.10.2022

TIME/ DAY	10.00am- 11.30am	11.30am-1.00pm	2.00pm- 3.30pm	3.30Ppm- 4.00pm	4.00pm- 5.00pm
MON	MCT	ESD			
TUE		ESD			
WED	DSD	EP LAB(12PM-1PM)	RM & IPR		ES LAB
THU	EP	MCT	EP	DSD	
FRI	EP LAB		ES LAB		
SAT			AC-1		

Subjects: Faculty

ESD: Embedded System Design: Dr. M. Asha Rani

EP: Embedded Programming: Dept. of CSE

DSD: Digital Systems Design: Mr. A. Shravan Kumar

MCT: Modern Control Theory: Dept. of EEE

ES Lab: Embedded System Lab: Mr. A. Shravan Kumar /Mr. C. Vinay Kumar (Guest Faculty)

EP Lab: Embedded Programming Lab: Dept. of CSE

RM & IPR: Research Methodology and IPR: Dr. L. Pratap Reddy

AC-1: English for Research Paper Writing: Dr. P. Dasharatham


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