

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II B. Tech (Regular) II SEMESTER – 2021-22

Room No: S-6

TIME TABLE

w.e.f: 28.03.2022

TIME/ DAY	10.00am- 11.30am	11.30pm- 12.00pm	12.00pm- 1.00pm	2.00pm- 3.00pm	3.00pm- 3.30pm	3.30pm- 4.00pm	4.00pm- 5.00pm
MON	EFW	APC		M-III			
TUE	M-III	ADC			LDIC/ADC LAB		
WED	APC	ADC		LDIC			
THU	EFW	LDIC		ADC (T)	Indian Constitution		
FRI	LDIC/APC LAB		M-III(T)	ADC/ APC LAB			APC (T)
Subjects: Faculty:							

Subjects: Faculty

M-III: Mathematics-III: Dept. of Mathematics

EFW: Electromagnetic Fields and Waves: Mr. K. Narender Reddy

ADC: Analog and Digital Communications: Mrs. Ch. Swapna

LDIC: Linear and Digital Integrated Circuits: Mrs. T. MadhaviKumari

APC: Analog and Pulse Circuits: Mrs. J. Samrajyam

ADC Lab: Analog and Digital Communications Lab: Mrs. B. Manasa/ Mrs. Ch. Swapna

LDIC Lab: Linear and Digital Integrated Circuits Lab: Mrs. T. MadhaviKumari/Dr. D. Karunakar Reddy/Mr. M. Rajesh

APC Lab: Analog and Pulse Circuits Lab: Mrs. J. Samrajyam /Mrs. Ch. Alekya

Indian Constitution: Dept. of HSS

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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II B. Tech (IDP) II SEMESTER – 2021-22
TIME TABLE

Room No: S-2

w.e.f: 28.03.2022

TIME/ DAY	10.00am- 11.30am	11.30pm- 12.00pm	12.00pm- 1.00pm	2.00pm- 3.00pm	3.00pm- 3.30pm	3.30pm- 4.00pm	4.00pm- 5.00pm
MON	LDIC LAB		M-III(T)	LDIC			ADC(T)
TUE	APC	LDIC		EFW		M-III	
WED	ADC		APC (T)	ADC LAB			
THU	APC				Indian Constitution		
FRI	M-III	EFW		ADC			
SAT	APC LAB						

Subjects: Faculty

M-III: Mathematics-III: Dept. of Mathematics

EFW: Electromagnetic Fields and Waves: Mr. K. Narender Reddy

ADC: Analog and Digital Communications: Dr. B. N. Bhandari

LDIC: Linear and Digital Integrated Circuits: Mrs. T. MadhaviKumari

APC: Analog and Pulse Circuits: Mrs. J. Samrajyam

ADC Lab: Analog and Digital Communications Lab: Dr. B. N. Bhandari /Mrs. B. Manasa/

Mrs. Ch. Swapna

LDIC Lab: Linear and Digital Integrated Circuits Lab:Dr. D. Karunakar Reddy/Mr. A. Shravan Kumar

APC Lab: Analog and Pulse Circuits Lab: Mrs. J. Samrajyam /Mrs. Ch. Alekya

Indian Constitution: Dept. of HSS

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JNTUH COLLEGE OF ENGINEERING, HYDERABAD

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

III B. Tech (Regular) II SEMESTER – 2021-22

TIME TABLE

Room No: F-2

w.e.f : 07.03.2022

TIME/ DAY	10.00am-11.30am	11.30pm-1.00pm	2.00pm-3.30pm	3.30pm-5.00pm
MON	AP	DSP(T-1hr)	VLSI Lab/ AC Lab (2pm-4pm)	
TUE	CMC	VLSID	VLSI Lab/ DSP Lab	
WED	DSP Lab/ AC Lab (10am-12pm)		CMC	DSP
THU	VLSID	AP	OE-1	VLSI (T-1hr)
FRI	DSP	OE-1	AP (T-1hr)	

Subjects: Faculty:

AP: Antennas and Propagation: Mr. M. Rajesh

DSP: Digital Signal Processing: Mr. M. Kanaka Chary

VLSID: VLSI Design: Mr. M. Sampath

CMC: Cellular and Mobile Communications: Mrs. CH. Alekya

OE-1: System Design through IoT: Mr. M. Rama Rao (Guest Faculty)

VLSI Lab: VLSI Design Lab: Mr. M. Sampath/ Mr. A. Shravan Kumar

AC Lab: Advanced Communications Lab: Mr. M. Rajesh/ Mr. K. Narender Reddy

DSP Lab: Digital Signal Processing Lab: Mr. M. Kanaka Chary/ Mr. Ch. Anil Kumar (Guest Faculty)


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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

III B. Tech (IDP & IDDMP) II SEMESTER – 2021-22
TIME TABLE

Room No: F-6

w.e.f : 07.03.2022

TIME/ DAY	10.00am-11.30am	11.30pm-1.00pm	2.00pm-3.30pm	3.30pm-5.00pm
MON	CMC	VLSID	DSP	DSP(T-1hr)
TUE	DSP Lab/ AC Lab (10Am – 12Pm)		AP	
WED	VLSID	AP (12pm-1pm)	VLSI Lab/ AC Lab (2 pm – 4pm)	
THU	DSP	CMC	OE-1	AP
FRI	VLSI (T-1hr)	OE-1	VLSI Lab/ DSP Lab	

Subjects: Faculty:

AP: Antennas and Propagation: Mr. M. Rajesh

DSP: Digital Signal Processing: Mr. M. Kanaka Chary

VLSID: VLSI Design: Mr. M. Sampath

CMC: Cellular and Mobile Communications: Mrs. CH. Alekya

OE-1: System Design through IoT: Mr. M. Rama Rao (Guest Faculty)

VLSI Lab: VLSI Design Lab: Mr. M. Sampath/ Mr. A. Shravan Kumar

AC Lab: Advanced Communications Lab: Mr. M. Rajesh/ Mr. K. Narender Reddy

DSP Lab: Digital Signal Processing Lab: Mr. M. Kanaka Chary/ Mr. Ch. Anil Kumar (Guest Faculty)


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JNTUH COLLEGE OF ENGINEERING, HYDERABAD

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

IV B. Tech (Regular) II SEMESTER – 2021-22

TIME TABLE (modified- 01/02/2022)

Room No: T-2

w.e.f : 24.01.2022

TIME/ DAY	10.00am-11.30am	11.30pm-1.00pm	2.00pm-3.30pm	3.30pm-5.00pm
MON				
TUE				
WED				
THU	SC	RS	OE-III	
FRI	RS	SC	OE-III	

Subjects: Faculty:

RS: Radar Systems: Mr. A. Shravan Kumar

SC: Satellite Communications: Mrs. B. Manasa

OE-III: Principles of Communications: TA


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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

IV B. Tech (IDP) II SEMESTER – 2021-22
TIME TABLE

Room No: F-9

w.e.f : 24.01.2022

TIME/ DAY	10.00am-11.30am	11.30pm-1.00pm	2.00pm-3.30pm	3.30pm- 4.00pm	4.00pm - 5.00pm
MON	ASP	ADSP	TA Lab		
TUE	ADC	TCP/IP	RM &IPR		
WED	ADC	ASP	TA Lab		
THU		CTT	TCP/IP		
FRI		CTT	ADSP		

Subjects: Faculty:

ADSP: Advanced Digital Signal Processing: Dr. K. Anitha sheela

ASP: Adaptive Signal Processing: Dr. A. Rajani

ADC: Advanced Data Communications: Mrs. B. Manasa

TCP/IP: TCP/IP and ATM Networks: Dr. P. Chandra Shekhar Reddy

CTT: Coding Theory and Techniques: Dr. T. Satya Savithri

RM &IPR: Research Methodology and IPR: Dr. A. Rajani

TA Lab: Technology Applications Lab: Mrs. J. Samrajyam/ Mr. Ch. Anil Kumar (Guest Faculty)


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