

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
V B. Tech (IDP) I SEMESTER – 2017-18
TIME TABLE

Room No: S-12

w.e.f:03.07.2017

	9.40 am to 11.20	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON				
TUE				
WED	WCN	NSC	ASP	
THU	NSC	CTT	SEMINAR	
FRI	WCN	ASP	CTT	

Subjects:

WCN: Wireless Communications & Networks

ASP: Adaptive Signal Processing

CTT: Coding Theory and Techniques

NS&C: Network Security & Cryptography

Seminar

Faculty:

: Dr. B.N. Bhandari

: Dr. M. Madhavi Latha

: Mr. M. Rajesh

: Mrs. T. Madhavi Kumari

: Dr. T. Satya Savithri/

Mr. D. Karunaker Reddi

Sally's
H.E.C.E.D
29/6/17

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
IV / IV B. Tech (ECE) (Regular) I SEMESTER, 2017-18

TIME TABLE

Room No: T-2

w.e.f: 03.07.2017

	9.40 am to 10.30 am	10.30 am to 11.20	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON	CMC		CN	VLSI	
TUE	SEMINAR			VLSI & E-CAD/MWE LAB	
WED	MWE		NSC	CN	
THU	NSC		CMC	VLSI	
FRI		VLSI & E-CAD/MWE LAB		MWE	

Subjects:

MWE: Microwave engineering

CN: Computer Networks

VLSI: VLSI Design

NS&C: Network Security & Cryptography

CMC: Cellular and Mobile Communications

VLSI& e-CAD Lab:

Microwave engineering Lab:

Seminar

Faculty:

Mr. C. Bharatender Rao

Dr. P. Chandra Sekhar Reddy

Mr. P. Shravan Kumar

Mrs. T. Madhavi Kumari

Mrs. B. Manasa

Dr. M. Asha Rani/

Mr. P. Shravan Kumar

Dr. T. Satya Savithri/

Mr. K. Narender Reddy

Dr. L. Pratap Reddy/

Mr. M. Rajesh

Sally
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 1/7/17

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

IV B. Tech (ECE) (IDP) I SEMESTER, 2017-18

TIME TABLE

Room No: T-8

w.e.f: 03.07.2017

	9.40 am to 10.30 am	10.30 am to 11.20 am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON	RS		CN	ACN LAB	
TUE	RS		CMC	MWE	
WED	VLSITD			CN	
THU	ADSP		MWE	MWE LAB	
FRI	CMC		ADSP	VLSITD	

Subjects:

MWE: Microwave engineering

CMC: Cellular and Mobile Communications

CN: Computer Networks

ADSP: Advanced Digital Signal Processing

RS: Radar Systems

VLSIT&D: VLSI Technology and Design

Microwave engineering Lab:

Advanced Communications and Networking Lab

Faculty:

Mr. C. Bharatender Rao

Mrs. B. Manasa

Dr. P. Chandra Sekhar Reddy

Mr. M. Kanaka Chary

Mr. A. Shravan Kumar

Dr. L. Pratap Reddy

Mr. C. Bharatender Rao/

Mr. K. Narender Reddy

Dr. D. Sreenivasa Rao/ Ms. B.Manasa

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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATIONS ENGINEERING
IV B. Tech (ECE) (IDDMP- SP) I SEMESTER, 2017-18

TIME TABLE

Room No: 7-7.

w.e.f: 03.07.2017

	9.40 am to 10.30 am	10.30 am to 11.20	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON					
TUE					
WED	VLSITD		NSC	TCP/IP	
THU	NSC		TCP/IP		
FRI	SP LAB			VLSITD	

Subjects:

VLSIT&D: VLSI Technology and Design

NS&C: Network Security & Cryptography

TCP/IP: TCP/IP Internetworking

SP Lab:

Faculty:

Dr. L. Pratap Reddy

Mrs. T. Madhavi Kumari

Dr. D. Sreenivasa Rao

TA1/TA2

*All Students are shifted
to Telecommunications*

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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATIONS ENGINEERING

IV B. Tech (ECE) (IDDMP- TS) I SEMESTER, 2017-18

TIME TABLE (Revised)

Room No: T-8

w.e.f : 01.08.2017

	9.40 am to 10.30 am	10.30 am to 11.20	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON					
TUE					
WED	VLSITD		C++	TCP/IP	NETWORKS LAB
THU			TCP/IP	C++	
FRI				VLSITD	

Subjects:

VLSIT&D: VLSI Technology and Design

C++: C++ Programming

TCP/IP: TCP/IP Internetworking

Networks Lab

Faculty:

Dr. L. Pratap Reddy

Ms. G. Sony (Guest Faculty)

Dr. D. Sreenivasa Rao

Dr. D. Sreenivasa Rao/

Mrs. B. Manasa


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1/8/17

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATIONS ENGINEERING

IV B. Tech (ECE) I SEMESTER, 2017-18

(IIDDMP- Information and Communication Technology)

TIME TABLE

Room No: 7-7.

w.e.f: 03.07.2017

	9.40 am to 10.30 am	10.30 am to 11.20	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON					
TUE					
WED			NSC	TCP/IP	
THU		NSC	TCP/IP	WT	
FRI		WT LAB		WT	

Subjects:

WT: Web Technology

NSC: Network Security and Cryptography

TCP/IP: TCP/IP Internetworking

Web Technologies Lab

Faculty:

Dept. of CSE

Mrs. T. Madhavi Kumari

Dr. D. Sreenivasa Rao

Dept. of CSE

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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
III / IV B. Tech (ECE) (Regular) I SEMESTER, 2017-18

TIME TABLE

Room No: F1

w.e.f:03.07.2017

	9.40 am to 10.30 am	10.30 am to 11.20am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON		DC/LIC LAB		AWP	
TUE	LDICA		MEFA	LIC/ DESIGN LAB	
WED		DC/DESIGN LAB		EMI	
THU	MEFA		AWP	DC	
FRI	DC		EMI	LDICA	

Subjects:

LDICA: Linear & Digital IC Applications

AWP: Antenna & Wave Propagation

DC: Digital Communications

OE-1: EMI

MEFA: Managerial Economics & Financial Analysis

LIC Lab: Linear IC Applications. Lab

DC Lab: Digital Communications Lab

Design Lab

Faculty:

Mrs. GS Divya

Mr. K. Narender Reddy

Mrs. N. Mangala Gouri

LI

Dept. of HSS

Mrs. T. Madhavi Kumari /

Mrs G.S. Divya

Dr. M. Madhavi Latha /

Mr. M. Kanaka Chary

Mr. M. Rajesh/

Mr. A. Shravan Kumar

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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATIONS ENGINEERING
III / IV B. Tech (ECE) (IDP&IDDMP) I SEMESTER, 2017-18

TIME TABLE ✓

Room No: F6

w.e.f:03.07.2017

	9.40 am to 10.30 am	10.30 am to 11.20am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON			DC		
TUE		AWP	DC		DC/LIC LAB
WED		LDICA	AWP	MEFA	
THU			LIC/DESIGN LAB	EMI	
FRI		LDICA	EMI	MEFA	
					DC/DESIGN LAB

Subjects:

LDICA: Linear & Digital IC Applications

AWP: Antenna & Wave Propagation

DC: Digital Communications

OE-1: EMI

MEFA: Managerial Economics & Financial Analysis

LIC Lab: Linear IC Applications. Lab

DC Lab: Digital Communications Lab

Design Lab

Faculty:

Mrs. GS Divya

Mr. K. Narender Reddy

Mrs. N. Mangala Gouri

L1

Dept. of HSS

Mrs. T. Madhavi Kumari /

Mrs G.S. Divya/TA

Mrs. N. Mangala Gouri /

Mr. M. Kanaka Chary/TA

Mr. A. Shravan Kumar/

Mr. K. Narender Reddy/Ms. Ashwini

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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
II / IV B. Tech (ECE) (Regular) I SEMESTER, 2017-18

TIME TABLE ✓

Room No: S2

w.e.f :17.07.2017

DAY/ TIME	9.40am – 10.30am	10.30 am – 11.20 am	11.20am – 1.00pm	2.00pm -3.40pm	3.40pm – 4.30pm
MON	AE		NA	ET/BS LAB	
TUE	ET	ET/AE LAB		AE	
WED	M-III		SS	AE/BS LAB	
THURS	NA		SS	M-III	
FRI	ET	GS LAB		ET	
SAT	NA	SS			

Subjects:

M-III: Mathematics -III
 SS: Signals & Systems
 ET: Electrical Technology
 AE: Analog Electronics
 NA: Network Analysis
 ET Lab: Electrical Technology Lab
 AE LAB: Analog Electronics lab-I
 BS LAB: Basic Simulation Lab
 GS Lab: Gender Sensitization Lab

Faculty:

Dept of Mathematics
 Dr. K. Anitha Sheela
 Mr. G. Chandrashekar
 Mrs. Ch.Alekya
 Mr. B. Yadagiri(EEE)
 Dr. A. Raghuram/ Mr. G. Chandrashekar
 Mrs. Ch. Alekya/ Mr. M. Sampath
 Dr. K. Anitha Sheela / Mrs. Himabindu
 Dept. of HSS

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 31/7/17

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
II / IV B. Tech (ECE) (IDP & IDDMP) I SEMESTER, 2017-18

TIME TABLE ✓

Room No: S6

w.e.f :31.07.2017

DAY/ TIME	9.40am – 10.30am	10.30 am – 11.20 am	11.20am – 1.00pm	2.00pm -3.40pm	3.40pm – 4.30pm
MON	NA		SS	GS LAB	
TUE	ET		SS	ET (IDP)/BS LAB(IDDMP-F12)	
WED	ET (IDDMP)/AE LAB(IDP)		NA		
THURS	M-III		AE	AE(IDDMP)/BS LAB(IDP)	
FRI	ET		M-III	AE	
SAT	NA	SS			

Subjects:

M-III: Mathematics -III

SS: Signals & Systems

ET: Electrical Technology

AE: Analog Electronics

NA: Network Analysis

ET Lab: Electrical Technology Lab

AE LAB: Analog Electronics lab-I

BS LAB: Basic Simulation Lab

GS Lab: Gender Sensitization Lab

Faculty:

Dept of Mathematics

Dr. K. Anitha Sheela

Mr. K. Vivekanand (EEE)

Mrs. Ch.Alekya

Mrs. M. Vanisree (EEE)

Dr. G.N. Srinivas(IDP)/Dr. K.H.Phani
Sree(IDDMP) / Mrs. M. Vanisree

Mr. C. Bharatender Rao / Mrs. Ch. Alekya

Dr. K. Anitha Sheela / Mrs. Himabindu

Dept. of HSS

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