

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
M. Tech (DT) – SSP - I YEAR – I SEMESTER - 2018 – 19

TIME TABLE (Revised)

Room No: G5

w.e.f: 17/09/2018

DAY/ TIME	9.40A.M- 10.40A.M	10.40A.M- 11.40A.M	11.40A.M- 12.40P.M	L U N C H	1.40P.M- 2.40P.M	2.40P.M - 3.40P.M	3.40P.M- 4.40P.M	
MON		SP LAB				SP LAB		
TUE	RPQT		TT			ADSP		
WED	RPQT	SL Lab				RM&IPR		
THU		RSP				AC-I	ADSP	
FRI	RSP	TT				SL Lab		

Subjects: Faculty

TT: Transform Techniques: Dr. T. Satya Savithri

ADSP: Advanced Digital Signal Processing: Dr. M. Madhavi Latha

RPQT: Random Processes and Queuing Theory: Dr. B. N. Bhandari

RSP: Radar Signal Processing: Dr. A. Rajani

SP Lab-Room No.F14: Signal Processing Lab: Dr. K. Anitha Sheela/ Ms. B. Manasa

SL Lab-Room No.F12: Scripting Languages Lab: Dr. L. Pratap Reddy/ Mr. A. Shravan Kumar

RM&IPR: Research Methodology and IPR: Dr. L. Pratap Reddy

AC-I: English for Research Paper Writing: Dr. P. Dasharatham

Sally
H.E.C.E.D
 17/9/18

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
M. Tech (DT) – DSCE - I YEAR – I SEMESTER - 2018 – 19

TIME TABLE (Revised)

Room No: S-15

w.e.f:17/09/2018

DAY/ TIME	9.40A.M- 10.40A.M	10.40A.M- 11.40A.M	11.40A.M- 12.40P.M	L U N C H	1.40P.M- 2.40P.M	2.40P.M - 3.40P.M	3.40P.M- 4.40P.M
MON		DSD LAB			DSD LAB		
TUE	DSD	TCP/IP	AM		SL Lab		
WED	DSD	AM			RM&IPR		
THU	VLSITD	TCP/IP			AC-1		
FRI		SL Lab			VLSITD		DSD

Subjects: Faculty

DSD: Digital System Design with PLDs: Mr. A. Shravan Kumar

VLSITD: VLSI Technology and Design: Mr. M. Sampath

AM: Advanced Microcontrollers: Dr. M. Asha Rani

TCP/IP: TCP/IP and ATM Networks: Dr. D. Sreenivasa Rao

DSD Lab-Room No.S14: Digital System Design Lab: Dr. M. Madhavi Latha/ Mrs. G. Sarvani

SL Lab-Room No.F12: Scripting Languages Lab: Dr. K. Anitha Sheela/ Dr. A. Rajani

RM&IPR: Research Methodology and IPR: Dr. L. Pratap Reddy

AC-I: English for Research Paper Writing: Dr. P. Dasharatham

Sally
H.E.C.E.D
 19/9/18

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
M. Tech (DT) – ES - I YEAR – I SEMESTER - 2018 – 19

TIME TABLE (Revised)

Room No: F-9

w.e.f: 17/09/2018

DAY/ TIME	9.40A.M- 10.40A.M	10.40A.M- 11.40A.M	11.40A.M- 12.40P.M	L U N C H	1.40P.M- 2.40P.M	2.40P.M - 3.40P.M	3.40P.M- 4.40P.M	
MON	DCS					MC		
TUE	ES LAB		AM			SIMULATION LAB		
WED		AM				RM&IPR		
THU	ES LAB		ESD			AC-I		
FRI	MC	SIMULATION LAB				ESD		DCS

Subjects: Faculty

ESD: Embedded System Design: Dr. M. Asha Rani

DCS: Digital Control Systems: Mrs. Ch. Anjamma

AM: Advanced Microcontrollers: Dr. M. Asha Rani

MC: Mobile Computing: Mrs. Kalpana

ES Lab-Room No.F12: Embedded System Lab: Dr. L. Pratap Reddy/ Ms. P. Anoosha

Simulation Lab-Room No.S14: Dr. M. Asha Rani / Mr. M. Sampath

RM&IPR: Research Methodology and IPR: Dr. L. Pratap Reddy

AC-I: English for Research Paper Writing: Dr. P. Dasharatham

Sallys
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 17/9/18