

10/07/2018 - Final copy

COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
V B. Tech (IDP) I SEMESTER – 2018-19
TIME TABLE

Room No: G12/T8

w.e.f:18.06.2018

	9.40 am to 11.20	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON				
TUE				
WED	CTT	ASP	SEMINAR	
THU	WCN	CTT	NSC	
FRI	WCN	NSC	ASP	

Subjects:

Faculty:

WCN: Wireless Communications & Networks: Dr. B. N. Bhandari

ASP: Adaptive Signal Processing: Dr. M. Madhavi Latha

CTT: Coding Theory and Techniques: Ms. B. Manasa

NSC: Network Security & Cryptography: Mrs. T. Madhavi Kumari

Seminar: Mrs. T. Madhavi Kumari/ Mrs. Ch. Alekya


H.E.C.E.D

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

IV / IV B. Tech (Regular) I SEMESTER, 2018-19

TIME TABLE

Room No: T-2

w.e.f:18.06.2018

	9.40 am to 10.30 am	10.30 am to 11.20am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON	RS		WCN		
TUE	MWE		ESD	VLSI	
WED		MWE/ VLSI& e-CAD Lab		MWE/ VLSI& e-CAD Lab	
THU			MWE	WCN	
FRI	RS		ESD	VLSI	

Subjects:

Faculty:

MWE: Microwave engineering: Mr. C. Bharatender Rao

WCN: Wireless Communications & Networks: Dr. P. Chandra Sekhar Reddy

ESD: Embedded System Design: Dr. L. Pratap Reddy

RS: Radar Systems: Mr. A. Shravan Kumar

VLSI: VLSI Design: Mr. P. Shravan Kumar

VLSI& e-CAD Lab: Dr. T. Satya Savithri/ Mr. M. Sampath

Microwave engineering Lab: Mr. C. Bharatender Rao /Mr. K. Narender Reddy


HECED

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
IV B. Tech (IDP) I SEMESTER, 2018-19

TIME TABLE

Room No: T-8

w.e.f:18.06.2018

	9.40 am to 10.30 am	10.30 am to 11.20 am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON			TT	MWE	
TUE	VLSITD		RS	TCP/IP	
WED	RS		TCP/IP	ADSP	
THU	TT		ADSP	MWE LAB	
FRI	VLSITD		MWE	ADSP LAB	

Subjects:

Faculty:

MWE: Microwave engineering: Mr. C. Bharatender Rao

RS: Radar Systems: Dr. A. Rajani

ADSP: Advanced Digital Signal processing: Dr. K. Anitha Sheela

TT: Transform Techniques: Dr. T. Satya Savithri

VLSITD: VLSI Technology and Design: Mr. P. Shravan Kumar

TCP/IP: TCP/IP and ATM Networks: Dr. D. Sreenivasa Rao

Microwave engineering Lab: Mr. C. Bharatender Rao /Mr. K. Narender Reddy

Advanced Digital Signal processing Lab: Dr. K. Anitha Sheela/Mr. M. Kanaka Chary


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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
III / IV B. Tech (Regular) I SEMESTER, 2018-19
TIME TABLE

Room No: F1

w.e.f:18.06.2018

	9.40 am to 10.30 am	10.30 am to 11.20am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON	AWP		LDICA	LIC/DESIGN LAB	
TUE		LIC/DC LAB		MEFA	
WED	MEFA		LDICA	EMI	
THU		DC/DESIGN LAB		DC	
FRI	DC		EMI	AWP	

Subjects:

Faculty:

LDICA: Linear & Digital IC Applications: Mrs. J. Samrajyam

AWP: Antenna & Wave Propagation: Mr. K. Narender Reddy

DC: Digital Communications: Mr. M. Rajesh

OE-I: EMI: TA

MEFA: Managerial Economics & Financial Analysis: Dept. of HSS

LIC Lab: Linear IC Applications. Lab: Mrs. J. Samrajyam/ Mrs. GS Divya

DC Lab: Digital Communications Lab: Mr. M. Rajesh/Mr. D. Karunaker Reddi/

Mr. K. Narender Reddy

Design Lab: Mr. P. Shravan Kumar/ Mr. A. Shravan Kumar

Sallys
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 10/7/18

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATIONS ENGINEERING
III / IV B. Tech (IDP&IDDMP) I SEMESTER, 2018-19

TIME TABLE

Room No: F6

w.e.f:18.06.2018

	9.40 am to 10.30 am	10.30 am to 11.20am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON		DESIGN LAB		AWP	
TUE	MEFA		AWP	DC	
WED			DC	EMI	
THU	LDICA		MEFA	LIC LAB	
FRI	LDICA		EMI	DC LAB	

Subjects:

Faculty:

LDICA: Linear & Digital IC Applications: Mrs. J. Samrajyam

AWP: Antenna & Wave Propagation: Mr. K. Narender Reddy

DC: Digital Communications: Mr. M. Rajesh

OE-1: EMI: TA

MEFA: Managerial Economics & Financial Analysis: Dept. of HSS

LIC Lab: Linear IC Applications. Lab: Mrs. J. Samrajyam/ Mrs. GS Divya

DC Lab: Digital Communications Lab: Dr. A. Rajani/Mr. M. Rajesh

Design Lab: Mr. P. Shravan Kumar/ Mr. D. Karunaker Reddi

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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
II / IV B. Tech (Regular) I SEMESTER, 2018-19

TIME TABLE

Room No: S2

w.e.f : 02.07.2018

DAY/ TIME	9.40am – 10.30am	10.30 am –11.20 am	11.20am – 1.00pm	2.00pm -3.40pm	3.40pm – 4.30pm
MON	M-III		AE	GS LAB	
TUE	SS(T)	BS/ET LAB		AE/ET LAB	
WED	NA		SS	BS/AE LAB	
THURS	SS		ET	M-III	NA (T)
FRI	ET		AE	NA	

Subjects:

Faculty:

M-III: Mathematics –III: Dept of Mathematics

SS: Signals & Systems: Mr. M. Kanaka Chary

ET: Electrical Technology: Dept. of EEE

AE: Analog Electronics: Mr. N. Mangala Gouri

NA: Network Analysis: Dept. of EEE

ET Lab: Electrical Technology Lab: Dept. of EEE

AE LAB: Analog Electronics lab-I: Mr. N. Mangala Gouri /Mrs. Ch. Alekya/ Mrs. J. Samrajyam

BS LAB: Basic Simulation Lab: Mr. M. Kanaka chary/TA

GS Lab: Gender Sensitization Lab: Dept. of HSS

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10/7/18

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATIONS ENGINEERING
IV B. Tech (ECE) (IDDMP- TS) I SEMESTER, 2018-19

TIME TABLE (Revised)

Room No: T-8 /S12

w.e.f:18.06.2018

	9.40 am to 10.30 am	10.30 am to 11.20	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON					
TUE	Networks+ACN Lab			ACN	
WED					
THU				NSC	
FRI	ACN			NP	

Faculty:

Subjects:

ACN: Advanced Computer Networks: Ms. B. Manasa

NSC: Network Security & Cryptography: Mrs. T. Madhavi Kumari

NP: Network Programming: Dept. of CSE

Networks+ACN Lab: Mrs. T. Madhavi Kumari/ Ms. B. Manasa


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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II / IV B. Tech (ECE) (IDP & IDDMP) I SEMESTER, 2018-19

TIME TABLE

Room No: S6

w.e.f : 02.07.2018

DAY/ TIME	9.40am – 10.30am	10.30 am – 11.20 am	11.20am – 1.00pm	2.00pm -3.40pm	3.40pm – 4.30pm
MON	NA		SS	AE/ET LAB	
TUE	M-III		AE	BS LAB	
WED	SS (T)	AE LAB/ET LAB		ET	NA (T)
THURS	ET		AE	GS LAB	
FRI	SS		NA	M-III	

Subjects:

Faculty:

M-III: Mathematics –III: Dept of Mathematics

SS: Signals & Systems: Mr. M. Kanaka Chary

ET: Electrical Technology: Dept. of EEE

AE: Analog Electronics: Mr. N. Mangala Gouri

NA: Network Analysis: Dept. of EEE

ET Lab: Electrical Technology Lab: Dept. of EEE

AE LAB: Analog Electronics lab-I: Mrs. Ch. Alekya/Mr. P. Shravan Kumar

BS LAB: Basic Simulation Lab: Mr. M. Kanaka chary/TA

GS Lab: Gender Sensitization Lab: Dept. of HSS

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