

Final

**JNTUH COLLEGE OF ENGINEERING, HYDERABAD**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**IV B. Tech (IDP) II SEMESTER – 2018-19**  
**TIME TABLE**

**Room No: T8**

**w.e.f: 19-11-2018**

	9.40 am to 10.30 am	10.30 am to 11.20	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
<b>MON</b>					
<b>TUE</b>					
<b>WED</b>	ACN LAB			ASP	
<b>THU</b>	ACN		MS		
<b>FRI</b>	ACN		ASP	MS	

**Subjects: Faculty**

MS: Management Science: Dept. of HSS

ACN: Advanced Computer networks: Dr. B. N. Bhandari

ASP: Adaptive Signal Processing: Dr. M. Madhavi Latha

ACN Lab: Advanced Computer networks Lab: Dr. D. Sreenivasa Rao/ Ms. B. Manasa

*Sally*  
**H.E.C.E.D**  
13/11/18

**JNTUH COLLEGE OF ENGINEERING, HYDERABAD**

**JNTUH COLLEGE OF ENGINEERING, HYDERABAD**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**IV / IV B. Tech (Regular) II SEMESTER-2018-19**

**TIME TABLE**

Room No: T-2

D.C.C: 19-11-2018  
wef:31/12/2018

	9.40 am to 10.30 am	10.30 am to 11.20	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
<b>MON</b>					
<b>TUE</b>					
<b>WED</b>			MS		SEMINAR
<b>THU</b>		MS		PCCN	ES-Room No.MB:319
<b>FRI</b>		PCCN			ES-Room No.MB:319

**Subjects: Faculty**

**OE-III:**

PCCN: Principles of Computer Communications and Networks: Mr. D. Karunaker Reddy

MS: Management Science: Dept. of HSS

Seminar: Dr. T. Satya Savithri/ Mr. M. Sampath

ES: Environmental Science: Only for lateral entry students

*K. MS*  
HECED  
29/12/18

**JNTUH COLLEGE OF ENGINEERING, HYDERABAD**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

**III / IV B. Tech (Regular) II SEMESTER-2018-19**

**TIME TABLE**

*modified: 15.12.18*

**Room No: F2**

**w.e.f: 19-11-2018**

	9.40 am to 10.30 am	10.30 am to 11.20am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
<b>MON</b>	MPMC		DSP	AEL/DSP LAB	
<b>TUE</b>	DSD/ CN		DIP/CMC	DSP	
<b>WED</b>	MPMC		DIP/CMC	PEC	
<b>THU</b>	AEL/MPMC LAB				
<b>FRI</b>	DSD/ CN		PEC	DSP/MPMC LAB	

**Subjects: Faculty**

PEC: Principles of Electronic Communications (OE-II): Mrs. Ch. Alekya

DIP/CMC: Digital Image Processing: Dr. T. Satya Savithri

: Cellular and Mobile Communications: Mr. M. Rajesh

DSD/ CN: Digital System Design: Mrs. GS Divya

: Computer Networks: Ms. B. Manasa

MPMC: Microprocessors and microcontrollers: Mrs. J. Samrajyam

DSP: Digital Signal Processing: Mrs. A. Rajani

AEL Lab: Advanced English Language Communication Skills Lab: Dept. of HSS

DSP Lab: Digital Signal Processing Lab: Mrs. A. Rajani/ Mr. M. Kanaka Chary

MPMC Lab: Microprocessors and Microcontrollers Lab: Mrs. T. Madhavi Kumari/  
Mrs. J. Samrajyam/ Mrs. GS Divya

*Sallys*  
**HECED**  
*15/12/18*

**JNTUH COLLEGE OF ENGINEERING, HYDERABAD**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**III / IV B. Tech (IDP&IDDMP) II SEMESTER-2018-19**

**TIME TABLE**

Room No: F6

w.e.f: 19-11-2018

	9.40 am to 10.30 am	10.30 am to 11.20am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
<b>MON</b>		MPMC LAB		MPMC	
<b>TUE</b>	DSD/CN		DIP/CMC	DSP	
<b>WED</b>	DSP		DIP/CMC	PEC	
<b>THU</b>		DSP LAB			
<b>FRI</b>	DSD/CN		PEC	MPMC	
<b>SAT</b>					AEL LAB

**Subjects: Faculty**

PEC: Principles of Electronic Communications (OE-II): Mrs. Ch. Alekya

DIP/CMC: Digital Image Processing: Dr. T. Satya Savithri

: Cellular and Mobile Communications: Mr. M. Rajesh

DSD/ CN: Digital System Design: Mrs. GS Divya

: Computer Networks: Ms. B. Manasa

MPMC: Microprocessors and microcontrollers: Mr. D. Karunaker Reddy

DSP: Digital Signal Processing: Mr. M. Kanaka Chary

AEL Lab: Advanced English Language Communication Skills Lab: Dept. of HSS

DSP Lab: Digital Signal Processing Lab: Mr. M. Kanaka Chary/Mr. M. Rajesh

MPMC Lab: Microprocessors and Microcontrollers Lab: Dr. M. Asha Rani/Mr. D. Karunaker

Reddy/ Mrs.GS Divya

*Sally*  
**HECED**  
 15/12/18

**JNTUH COLLEGE OF ENGINEERING, HYDERABAD**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

**II / IV B. Tech (Regular) II SEMESTER-2018-19**

**TIME TABLE**

**Room No: S2**

**w.e.f: 03.12.2018**

<b>DAY/ TIME</b>	<b>9.40am – 10.30am</b>	<b>10.30 am – 11.20 am</b>	<b>11.20am – 1.00pm</b>	<b>2.00pm -3.40pm</b>	<b>3.40pm – 4.30pm</b>
MON	EMTL		CS	PDC	
TUE	CS		EMTL	HVPE	
WED	STLD		AC	AC/AE-II LAB	
THURS	AC/PDC LAB			AE-II/PDC LAB	
FRI	PDC		STLD	AC	

**Subjects: Faculty**

STLD: Switching Theory and Logic Design: Mrs. T. Madhavi Kumari

PDC: Pulse and Digital Circuits: Mr. A. Shravan Kumar

EMTL: Electromagnetic Theory and Transmission Lines: Mr.K.Narender Reddy

AC: Analog Communications: Dr. K. Anitha Sheela

CS: Control Systems: Dept. of EEE

HVPE: Human Values and Professional Ethics: Dept. of HSS

AC Lab: Analog Communications Lab:Dr.K.Anitha Sheela/Mr.K.Narender Reddy/ Ms. B. Manasa

PDC LAB:Pulse and Digital Circuits:Mr.C.Bharatender Rao/Mr.A.Shravan Kumar/Mrs.Ch.Alekya

AE-II LAB: Analog Electronics lab-II: Mr.P.Shravan/Mr.M. Kanaka Chary

  
**HECED**

**JNTUH COLLEGE OF ENGINEERING, HYDERABAD**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**II / IV B. Tech (ECE) (IDP) II SEMESTER-2018-19**  
**TIME TABLE**

Room No: S6

w.e.f: 03.12.2018

DAY/ TIME	9.40am - 10.30am	10.30 am – 11.20 am	11.20am – 1.00pm	2.00pm -3.40pm	3.40pm – 4.30pm
MON		PDC	HVPE	AC/PDC LAB	
TUE		CS	PDC	PDC/AE-II LAB	
WED		EMTL	STLD	AC	
THURS		CS	STLD	EMTL	
FRI		AC/AE-II LAB		AC	

**Subjects: Faculty**

STLD: Switching Theory and Logic Design: Mrs. N. Mangala Gouri

PDC: Pulse and Digital Circuits: Mrs.Ch. Alekya

EMTL: Electromagnetic Theory and Transmission Lines: Mr.K.Narender Reddy

AC: Analog Communications: Mr. M. Rajesh

CS: Control Systems: Dept. of EEE

HVPE: Human Values and Professional Ethics: Dept. of HSS

AC Lab: Analog Communications Lab: Mrs. T. Madhavi Kumari Mr. M. Rajesh/

Mr. K. Narender Reddy

PDC LAB: Pulse and Digital Circuits: Mr.C.Bharatender Rao/Mrs.Ch.Alekya/Mrs. GS Divya

AE-II LAB: Analog Electronics lab-II: Mrs. N. Mangala Gouri/Mrs.J.Samrajyam/Mr.P.Shravan

*Sally*  
**HECED**  
 15/12/18