

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
M. Tech (DT) – SSP - I YEAR – II SEMESTER - 2018 – 19

TIME TABLE

Room No: G5

w.e.f: 07/01/2019

DAY/ TIME	9.40A.M- 10.40A.M	10.40A.M- 11.40A.M	11.40A.M- 12.40P.M	L U N C H	1.40P.M- 2.40P.M	2.40P.M - 3.40P.M	3.40P.M- 4.40P.M	
MON	CN LAB		IVP		SEMINAR			
TUE	WCN		AWSN		ASP	CN LAB		
WED	WCN	SIMULATION LAB			SIMULATION LAB			
THU		ASP			VE		SEMINAR	
FRI		IVP			AWSN			

Subjects: Faculty

ASP: Adaptive Signal Processing: Dr. M. Madhavi Latha

WCN: Wireless Communications and Networks: Dr. B. N. Bhandari

IVP: Image and Video Processing: Dr. T. Satya Savithri

AWSN: Ad-hoc and Wireless Sensor Networks: Dr. D. Sreenivasa Rao

CN Lab (G12): Communications and Networks Lab: Mr. M. Rajesh/ Ms. B. Manasa

Simulation Lab (F14): Dr. A. Rajani/Mr. D. Karunaker Reddi

Mini Project with Seminar: Dr. T. Satya Savithri /Dr. K. Anitha Sheela

AC-2: Audit Course-2: VE: Value Education: Dept. of HSS

K. ABB
H.E.C.E.D 05/01/19

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
M. Tech (DT) – DSCE - I YEAR – II SEMESTER - 2018 – 19

TIME TABLE

Room No: S-15

w.e.f: 07/01/2019

DAY/ TIME	9.40A.M- 10.40A.M	10.40A.M- 11.40A.M	11.40A.M- 12.40P.M	L U N C H	1.40P.M- 2.40P.M	2.40P.M - 3.40P.M	3.40P.M- 4.40P.M	
MON		SIMULATION LAB				SEMINAR		
TUE	ES LAB		DFTS			ACA		
WED	RTOS	DFTS				ES LAB		SEMINAR
THU	LPVLSI		ACA			VE		
FRI	LPVLSI	RTOS				SIMULATION LAB		

Subjects: Faculty

ACA: Advanced Computer Architecture: Dr. P. Chandrashekar Reddy

DFTS: Design of Fault Tolerant Systems: Dr. M. Asha Rani

RTOS: Embedded Real Time Operating systems: Dr. L. Pratap Reddy

LPVLSI: Low Power VLSI Design: Mr. M. Sampath

ES Lab (F12): Embedded Systems Laboratory: Mrs. J. Samrajyam/ Mr. A. Shravan Kumar

Simulation Lab (S14): Dr. M. Madhavi Latha/ Mr. P. Shravan Kumar

Mini Project with Seminar: Dr. D. Sreenivasa Rao/Mrs. N. Mangala Gouri

AC-2: Audit Course-2: VE: Value Education: Dept. of HSS

K. Asha Rani
H.E.C.E.D
05/01/19

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
M. Tech (DT) – ES - I YEAR – II SEMESTER - 2018 – 19

TIME TABLE

Room No: F-9

w.e.f: 07/01/2019

DAY/ TIME	9.40A.M- 10.40A.M	10.40A.M- 11.40A.M	11.40A.M- 12.40P.M	L U N C H	1.40P.M- 2.40P.M	2.40P.M - 3.40P.M	3.40P.M- 4.40P.M
MON	EP	SL LAB			EP LAB		
TUE	SoCA		MCT		SL LAB		SEMINAR
WED	ERTOS	MCT			SEMINAR		
THU	EP		SoCA		VE		
FRI		ERTOS			EP LAB		

Subjects: Faculty

SoCA: System on Chip Architecture: Mr. P. Shravan Kumar
 EP: Embedded Programming : Mr. N. Sampath Kumar (Dept. of CSE)
 MCT: Modern Control Theory: Mr. P. Venkatanarayana (Dept. of EEE)
 ERTOS: Embedded Real Time Operating systems: Dr. L. Pratap Reddy
 EP Lab (F12): Embedded Programming Lab: Dept. of CSE
 SL Lab (F12): Scripting Languages Lab: Dr. L. Pratap Reddy/ Mr. A. Shravan Kumar
 Mini Project with Seminar: Dr. L. Pratap Reddy/ Dr. M. Asha Rani
 AC-2: Audit Course-2: VE: Value Education: Dept. of HSS

K. Asha Rani
H.E.C.E.D
 05/01/19