

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

V B. Tech (IDP) I SEMESTER – 2019-20
TIME TABLE

Room No: G12

w.e.f:17.06.2019

	9.40 am to 11.20	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON				
TUE	WCN	DSPA	CTT	
WED	WCN	DSPA	4G	
THU	4G	CTT	SEMINAR	
FRI				

Subject: Faculty

WCN: Wireless Communications & Networks: Dr. B. N. Bhandari

4G: 4G Technologies: Mr. K. Narender Reddy

CTT: Coding Theory and Techniques: Mr. M. Sampath

DSPA: Digital Signal Processors and Architectures: Dr. M. Madhavi Latha

Seminar: Mr. A. Shravan Kumar/Mr. M. Sampath


H.E.C.E.D

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
IV / IV B. Tech (ECE) (Regular) I SEMESTER, 2019-20

TIME TABLE

Room No: T-2

w.e.f: 17.06.2019

	9.40 am to 10.30 am	10.30 am to 11.20am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON		RS	ANN	MWE/VLSI e-CAD LAB	
TUE		SC	RS		
WED		SC	VLSI	MWE	
THU		MWE	ANN		
FRI			VLSI	MWE/VLSI e-CAD LAB	

Subjects:

Faculty:

MWE: Microwave engineering: Mr. M. Rajesh

ANN: Artificial Neural Networks: Dr. K. Anitha Sheela

SC: Satellite Communications: Ms. B. Manasa

RS: Radar Systems: Mr. A. Shravan Kumar

VLSI: VLSI Design: Dr. T. Satya Savithri

VLSI& e-CAD Lab: Dr. T. Satya Savithri/ Mr. M. Sampath

Microwave Engineering Lab: Mrs. J. Samrajyam/ Mrs. Ch. Alekya


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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
IV B. Tech (ECE) (IDP) I SEMESTER, 2019-20

TIME TABLE

Room No: T-8

w.e.f:17.06.2019

	9.40 am to 10.30 am	10.30 am to 11.20 am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON	ADSP		BMSP	MWE	
TUE	ADSP		ANN	TCP/IP	
WED	ANN		VLSITD	BMSP	
THU			TCP/IP	MWE LAB	
FRI	VLSITD		MWE	ADSP LAB	

Subjects:

Faculty:

MWE: Microwave engineering: Mr. M. Rajesh

ANN: Artificial Neural Networks: Dr. K. Anitha Sheela

ADSP: Advanced Digital Signal processing: Mr. M. Kanaka Chary

BMSP: Biomedical Signal Processing: Dr. A. Rajani

VLSIT&D: VLSI Technology and Design: Mr. A. Shravan Kumar

TCP/IP: TCP/IP and ATM Networks: Dr. D. Sreenivasa Rao

Microwave engineering Lab: Mrs. J. Samrajyam/ Mrs. Ch. Alekya

Advanced Digital Signal processing Lab: Mr. M. Kanaka Chary/Mr. B. Manasa



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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATIONS ENGINEERING
IV B. Tech (ECE) (IDDMP- TS) I SEMESTER, 2019-20

TIME TABLE

Room No: T-8

w.e.f:17.06.2019

	9.40 am to 10.30 am	10.30 am to 11.20	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON					
TUE					
WED			NSC	NP+ACN LAB	
THU		ACN	NP		
FRI		ACN	NSC	NP	

Subjects:

Faculty:

ACN: Advanced Computer Networks: Dr. P. Chandrasekhar Reddy

NS&C: Network Security & Cryptography: Mrs. T. Madhavi Kumari

NP: Network Programming: Dept. of CSE

NP+ACN Lab: Ms. B. Manasa/ Mr. N. Sampath (Dept. of CSE)


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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

III / IV B. Tech (ECE) (Regular) I SEMESTER, 2019-20

TIME TABLE

Room No: F1

w.e.f:17.06.2019

	9.40 am to 10.30 am	10.30 am to 11.20am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON		DC/DESIGN LAB		AWP	
TUE		LIC/DC LAB		MEFA	
WED	LDICA		DC	EMI(OE-I)	
THU	DC		LDICA	DESIGN/LIC LAB	
FRI	AWP		EMI(OE-I)	MEFA	

Subjects:

Faculty:

LDICA: Linear & Digital IC Applications: Mrs. GS. Divya

AWP: Antennas & Wave Propagation: Mr. K. Narendar Reddy

DC: Digital Communications: Mrs. N. Mangala Gouri

OE-I: EMI:

MEFA: Managerial Economics & Financial Analysis: Dr. P. Prabhakar(HSS)

LIC Lab: Linear IC Applications. Lab: T. Madhavi Kumari /Mrs. GS Divya/

Mr. B. Ravi Kumar(Guest Faculty)

DC Lab: Digital Communications Lab: Mrs. N. Mangala Gouri/ Mr. M. Rajesh/

Mr. Ch. Anil Kumar(Guest Faculty)

Design Lab: Mr. P. Shravan Kumar/Ms. B. Manasa


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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATIONS ENGINEERING
III / IV B. Tech (ECE) (IDP&IDDMP) I SEMESTER, 2018-19

TIME TABLE

Room No: F6

w.e.f:17.06.2019

	9.40 am to 10.30 am	10.30 am to 11.20am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON	AWP		LDICA	LIC/DESIGN LAB	
TUE	MEFA		AWP	LIC/DC LAB	
WED		DESIGN/DC LAB			EMI(OE-I)
THU			MEFA	DC	
FRI	DC		EMI(OE-I)	LDICA	

Subjects:

Faculty:

LDICA: Linear & Digital IC Applications: Mr. C. Bharatender Rao

AWP: Antennas & Wave Propagation: Mr. K. Narender Reddy

DC: Digital Communications: Mrs. N. Mangala Gouri

OE-I: EMI:

MEFA: Managerial Economics & Financial Analysis: Dr. P. Prabhakar(HSS)

LIC Lab: Linear IC Applications. Lab: Mr. C. Bharatender Rao /Mrs. GS Divya/

DC Lab: Digital Communications Lab: Dr. P. Chandasekhar Reddy/ Mr. M. Rajesh

Design Lab: Mr. D. Karunaker Reddi/Mr. M. Rama Rao (Guest Faculty)



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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
II / IV B. Tech (ECE-Regular) I SEMESTER, 2019-20

TIME TABLE

Room No: S2

w.e.f: 01.07.2019

w.e.f: 01.07.2019

DAY/ TIME	9.40am – 10.40am	10.40 am –11.40 am	11.40am – 12.40pm	L U N C H	1.40pm 2.40pm	2.40pm- 3.40pm	3.40pm 4.30pm
MON	PTSP		STLD		NTL		
TUE	EDC	DLD/EDC LAB			DLD/BS (T)LAB		
WED	EDC/BS(T) LAB		NTL				
THURS	SS	STLD			ES		
FRI	PTSP	EDC			SS		
SAT	BS LAB				BS LAB		

Subjects: Faculty

EDC: Electronic Devices and Circuits: Mrs. Ch. Alekya

NTL: Network Analysis & Transmission Lines: Dept. of EEE

SS: Signals and Systems: Dr. A. Rajani

STLD: Switching Theory and Logic Design: Dr. M. Asha Rani

PTSP: Probability Theory and Stochastic Processes: Dr. P. Laxmi Narayana

EDC Lab: Electronic Devices and Circuits Lab: Mrs. Ch. Alekya/ Mr. M. Naresh (Guest Faculty)

DLD Lab: Digital Logic Design Lab: Mr. D. Karunaker Reddi/

Mr. M. Rama Rao (Guest Faculty)

BS Lab: Basic Simulation Lab: Mr. K. Narender Reddy/ Mr. R. Priyakanth (Guest Faculty)

ES: Environmental Science: Mr. Ashok (Dept. of Chemistry)



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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
II / IV B. Tech (ECE-IDP) I SEMESTER, 2019-20

TIME TABLE

Room No: S6

w.e.f: 01.07.2019

DAY/ TIME	9.40am – 10.40am	10.40 am –11.40 am	11.40am – 12.40pm	L U N C H	1.40pm 2.40pm	2.40pm- 3.40pm	3.40pm 4.30pm
MON		NTL			STLD		
TUE	PTSP	EDC			STLD		
WED	SS		EDC		EDC/BS LAB		
THURS	NTL	DLD/BS LAB			PTSP		
FRI	SS	DLD/EDC LAB			ES		

Subjects: Faculty

EDC: Electronic Devices and Circuits: Mr. C. Bharatender Rao

NTL: Network Analysis & Transmission Lines: Dept. of EEE *Smt. Vanisree*

SS: Signals and Systems: Mr. M. Kanaka Chary

STLD: Switching Theory and Logic Design: Mrs. T. Madhavi Kumari

PTSP: Probability Theory and Stochastic Processes: Mr. Ch. Anil Kumar

EDC Lab: Electronic Devices and Circuits Lab: Mrs. GS Divya/Mr. M. Naresh (Guest Faculty)

DLD Lab: Digital Logic Design Lab: Mr. D. Karunaker Reddi/

Mr. M. Rama Rao (Guest Faculty)

BS Lab: Basic Simulation Lab: Mr. M. Kanaka Chary/ TA

ES: Environmental Science: Mr. Ashok (Dept. of Chemistry)

K. AB
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