

Original

J. J. COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
IV B. Tech (IDP) II SEMESTER – 2019-20
TIME TABLE

Room No: G12

w.e.f: 25-11-2019

	9.40 am to 10.30 am	10.30 am to 11.20	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON					
TUE		ACN		ASP	MS
WED		ACN		MS	ASP
THU					
FRI		ACN LAB			

Subjects: Faculty

MS: Management Science, Mr. Anjum Pasha-Dept. of HSS

ACN: Advanced Computer Networks: Mr. M. Rajesh

ASP: Adaptive Signal Processing: Dr. M. Madhavi Latha

ACN Lab: Advanced Computer Networks Lab: Dr. P. Chandra Sekhar Reddy/ Mr. M. Rajesh


H.E.C.E.D

JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
IV / IV B. Tech (Regular) II SEMESTER-2019-20

TIME TABLE

Room No: T-2

w.e.f: 25-11-2019

	9.40 am to 10.30 am	10.30 am to 11.20	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON					
TUE					
WED					SEMINAR
THU			MS	PCCN	
FRI		PCCN	MS		

Subjects: Faculty

OE-III:

PCCN: Principles of Computer Communications and Networks: NA

MS: Management Science: Mr. Anjum Pasha-Dept. of HSS

Seminar: Mr. C. Bharatender Rao/ Mr. D. Karunaker Reddi



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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

III / IV B. Tech (Regular) II SEMESTER-2019-20

TIME TABLE

Room No: F2

w.e.f: 25-11-2019

	9.40 am to 10.30 am	10.30 am to 11.20am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON	DSD		DSP	AEL/MPMC LAB	
TUE	DSD		MPMC	CMC/COOS	
WED		AEL/DSP LAB		OE-II	
THU			MPMC	DSP/MPMC LAB	
FRI	DSD		OE-II	CMC/COOS	

Subjects: Faculty

PEC: Principles of Electronic Communications (OE-II):

CMC/COOS: Cellular and Mobile Communications: Dr. P. Chandra Sekhar Reddy

: Computer Organization and Operating Systems: Mrs. T. Madhavi Kumari

DSD: Digital System Design: Mrs. GS Divya

MPMC: Microprocessors and Microcontrollers: Dr. M. Asha Rani

DSP: Digital Signal Processing: Mr. M. Kanaka Chary

AEL Lab: Advanced English Language Communication Skills Lab: Dept. of HSS

DSP Lab: Digital Signal Processing Lab: Dr. A. Rajani/ Mr. M. Kanaka Chary

MPMC Lab: Microprocessors and Microcontrollers Lab: Mrs. J. Samrajyam/ Mrs. GS Divya


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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
III / IV B. Tech (IDP&IDDMP) II SEMESTER-2019-20

TIME TABLE

Room No: F6

w.e.f: 25-11-2019

	9.40 am to 10.30 am	10.30 am to 11.20am	11.20 am to 1.00 pm	2.00 pm to 3.40 pm	3.40 pm to 4.30 pm
MON	MPMC		DSD	DSP LAB	
TUE	DSD		DSP	CMC/COOS	
WED		MPMC LAB		OE-II	
THU		AEL LAB		MPMC	
FRI	DSP		OE-II	CMC/COOS	

Subjects: Faculty

PEC: Principles of Electronic Communications (OE-II):

CMC/COOS: Cellular and Mobile Communications: Dr. P. Chandra Sekhar Reddy

: Computer Organization and Operating Systems: Mrs. T. Madhavi Kumari

DSD: Digital System Design: Mr. P. Shravan Kumar

MPMC: Microprocessors and microcontrollers: Mr. D. Karunaker Reddy

DSP: Digital Signal Processing: Dr. A. Rajani

AEL Lab: Advanced English Language Communication Skills Lab: Dept. of HSS

DSP Lab: Digital Signal Processing Lab: Mr. K. Narender Reddy/Mr. M. Rajesh

MPMC Lab: Microprocessors and Microcontrollers Lab: Mr. D. Karunaker Reddi/

Mrs. J. Samrajyam


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JNTUH COLLEGE OF ENGINEERING, HYDERABAD
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II / IV B. Tech (Regular) II SEMESTER-2019-20

TIME TABLE

Room No: S2

w.e.f: 09.12.2019

DAY/ TIME	9.40am – 10.40am	10.40am- 11.40am	11.40am – 12.40pm	1.40pm - 2.40pm	2.40pm- 3.40pm	3.40pm – 4.40pm
MON	ADC	EFW		M-III		
TUE	Indian Constitution		EFW	APC	ADC/LDIC LAB	
WED	LDIC	LDIC/APC LAB		M-III	M-III(T)	APC(T)
THURS	APC	LDIC		APC/ADC LAB		ADC(T)
FRI	ADC		APC			

Subjects: Faculty

M-III: Mathematics-III: Dept. of Mathematics

EFW: Electromagnetic Fields and Waves: Mr. K. Narender Reddy

ADC: Analog and Digital Communications: Ms. B. Manasa

LDIC: Linear and Digital Integrated Circuits: Mrs. GS Divya

APC: Analog and Pulse Circuits: Mrs. Ch. Alekya

ADC Lab: Analog and Digital Communications Lab: Dr. K. Anitha Sheela/ Ms. B. Manasa/

Mr. K. Narender Reddy

LDIC Lab: Linear and Digital Integrated Circuits Lab: Dr. T. Satya Savithri/Mrs. GS Divya/
Mr. M. Sampath

APC Lab: Analog and Pulse Circuits Lab: Mr. C. Bharatender Rao/ Mrs. Ch. Alekya/ Mr. A.
Shravan Kumar

Indian Constitution: Dept. of HSS



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
II / IV B. Tech (IDP &IDDMP) II SEMESTER-2019-20

TIME TABLE

Room No: S6

w.e.f: 09.12.2019

DAY/ TIME	9.40am – 10.40am	10.40am- 11.40am	11.40am – 12.40pm	1.40pm - 2.40pm	2.40pm- 3.40pm	3.40pm – 4.40pm
MON	LDIC	APC		M-III		
TUE		ADC	M-III	Indian Constitution		
WED	APC	EFW		ADC/LDIC LAB		ADC(T)
THURS	ADC	LDIC/APC LAB		M-III(T)		
FRI	EFW	LDIC		ADC/APC LAB		APC(T)

Subjects: Faculty

M-III: Mathematics-III: Dept. of Mathematics

EFW: Electromagnetic Fields and Waves: Mr. K. Narender Reddy

ADC: Analog and Digital Communications: Ms. B. Manasa

LDIC: Linear and Digital Integrated Circuits: Mrs. J. Samrajyam

APC: Analog and Pulse Circuits: Mrs. Ch. Alekya

ADC Lab: Analog and Digital Communications Lab: Dr. K. Anitha Sheela / Mr. K. Narender
Reddy

LDIC Lab: Linear and Digital Integrated Circuits Lab: Mrs. T. Madhavi Kumari / Mr. M. Rajesh

APC Lab: Analog and Pulse Circuits Lab: Mr. C. Bharatender Rao/Mrs. Ch. Alekya

Indian Constitution: Dept. of HSS


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