Registration Form Name of the Applicant: (in Block Letters) Gender (M/F): Educational Qualification: Designation:

Teaching Experience:

Research Experience:

Name of College:

Reason for attending the workshop:

Address for correspondence:

Contact Number: Email ID:

Signature of the Applicant with date

Signature of Head of Institution with seal

Patron:

Prof. Rameshwar Rao Vice Chancellor, JNTUH

Co-Patron:

Dr. N. Yadaiah

Principal I/C, JNTUH CEH

Advisory Committee

Prof. P.V.D. Somasekhar Rao

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Mrs. P. SowjanyaSridevi

Address for Correspondence

Mr. P. Udhay Prakash

Department of ECE, JNTUH CEH,

Kukatpally, Hyderabad.

Cell: +91-9533787794.

Email: uday3prakash[at]gmail.com

Two-day Workshop on Antenna Simulation using ANSYS HFSS Tools (under TEOIP II)

May 2-3, 2014

Organized by



Department of E.C.E
JNTUH College of Engineering
Hyderabad, INDIA

Coordinators

Dr. B.N. Bhandari Professor

&

Dr. M.Madhavi Latha Professor and Director, ITC

In collaboration with



Entuple Technologies, Bangalore

Sponsored by TEQIP-II

Jawaharlal Nehru Technological University,

the First Technological University of India, was established on 2nd October 1972 in Andhra Pradesh with headquarters located in a historic city Hyderabad. The University is one of the premier Universities in India accredited by NAAC with 'A' Grade. After successful and proven levels of appreciated existence and stature spanning over 36 years, JNTU has been divided into four different universities by Govt. of Andhra Pradesh, through Act No.30, Dt. 24th September, 2008. One of the constituent colleges of the University "JNTUH College of Engineering, Hyderabad" is regarded as a pioneer in shaping the excellence of some of the leading organizations of the industry, by churning out the finest professionals with a resolve to scale greater heights in the technological scenario, every year.

The Department of **Electronics and Communication Engineering,** established in 1973, is instrumental in molding the careers of students and helping them to become world-class professionals. Besides highly qualified and experienced staff and well-equipped laboratories, the Department has been awarded **8.1** points out of **10** by the **State Board of Technical Education.**

About Entuple:

Entuple Technologies is Head quartered in Bangalore with offices in 8 major cities across the country, focused in Communications, VLSI design, Embedded Systems, RF & Microwave, PCB design, Drives and power electronics. Closely Work with major corporate, defence & education. Entuple is committed to bridge the ever growing gap between Academia and industry by bringing in expertise to meet technological challenges by introducing cutting edge platforms, tools and solutions.

About the Workshop Programme:

The objective of the workshop is to provide an insight on the utility of simulation tools for finalizing the Antenna Design and RF Circuit Design. The workshop provides hands-on experience in using ANSYS HFSS simulator tools, leading to the design of patch antennas and other microwave antennas, System Receiver simulations and microstrip antenna fabrication features. The two-day programme will enable the participants to understand the simulation principles and optimization procedures in the identified topics.

Topics include:

- Introduction to ANSYS HF Packages
- Hands on Training on micro-strip patch antenna
- Hands on Training on Dipole antenna array
- O Hands on Training on Horn antenna fed Reflector system
- Hands on training on System receiver simulation (ANSYS Designer)
- Antenna Fabrication
- Project / specific Query discussion

Resource Persons:

Resource persons from Entuple Technologies

Eligibility:

Faculty, Research Scholars, PG students and Industry personnel working in the relevant area. Intake is likely to be limited to 30.

Important Dates:

Last date for registration: 01-05-2014

Programme Timings: 9.30 am to 5.30 pm

How to Apply:

Interested participants may fill the prescribed registration form and mail the scanned form **strictly** on or before **01-05-2014**.

Registration Fee:

An amount of Rs. 500/- must be paid in DD drawn in favor of Coordinator, ASAHT, Dept. of ECE, JNTUH CEH payable at Hyderabad.

Venue:

Dept. of ECE, JNTUH CE, Kukatpally

Accommodation:

Limited accommodation will be provided on first come-first serve basis, on payment in University Guest House.