

**JNTUH COLLEGE OF ENGINEERING: HYDERABAD**  
(AUTONOMOUS)

**B.Tech. III year I Semester, Supplementary Examinations,**  
**November, 2015**

**08' Batch**

**EXAMINATION TIME – TABLE**

**Exam Time: 02.00PM to 05.00 PM**

<b>Date / Day</b>	<b>ECE</b>	<b>CSE</b>
16-11-2015	Computer Organisation	-
18-11-2015	Linear IC Applications	-
20-11-2015	Digital IC Applications	Computer Networks
23-11-2015	Antennas & Wave Propagation	Formal Languages & Automata Theory
25-11-2015	Digital Communications	-

**Date:09-11-2015**

**PRINCIPAL**